

### FEATURES

- Double Side Cooling
- High Surge Capability

### APPLICATIONS

- Medium Voltage Soft Starts
- High Voltage Power Supplies
- Static Switches

### KEY PARAMETERS

$V_{DRM}$	<b>6500V</b>
$I_{T(AV)}$	<b>840A</b>
$I_{TSM}$	<b>12000A</b>
$dV/dt^*$	<b>1500V/<math>\mu</math>s</b>
$dI/dt$	<b>200A/<math>\mu</math>s</b>

\* Higher  $dV/dt$  selections are available on request

### VOLTAGE RATINGS

Part and Ordering Number	Repetitive Peak Voltages $V_{DRM}$ and $V_{RRM}$ (V)	Conditions
DCR820N65*	6500	$T_{vj} = -40^{\circ}\text{C}$ to $125^{\circ}\text{C}$ , $I_{DRM} = I_{RRM} = 200\text{mA}$ , $V_{DRM}, V_{RRM} t_p = 10\text{ms}$ $V_{DSM} \& V_{RSM} =$ $V_{DRM} \& V_{RRM} + 100\text{V}$ respectively
DCR820N60	6000	
DCR820N55	5500	
DCR820N50	5000	

Lower voltage grades available.

\*6200V @  $-40^{\circ}\text{C}$ , 6500V @  $0^{\circ}\text{C}$

### ORDERING INFORMATION

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

#### DCR820N65

Note: Please use the complete part number when ordering and quote this number in any future correspondence relating to your order.

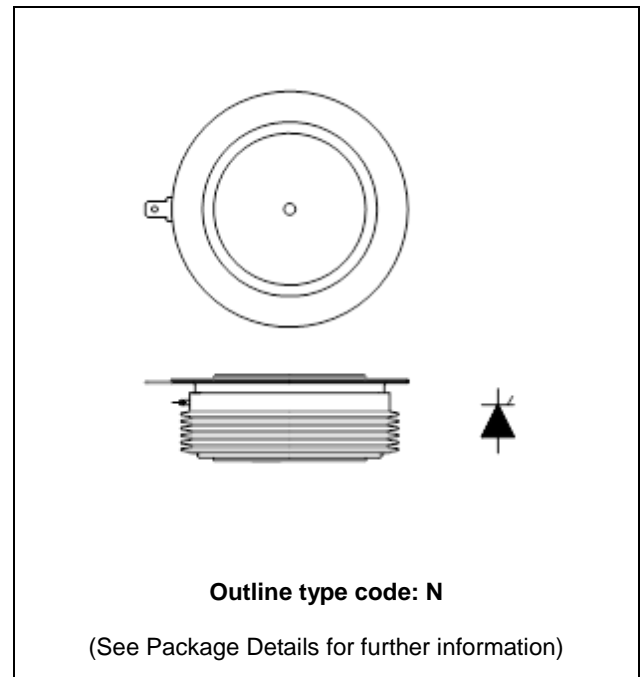


Fig. 1 Package outline

## CURRENT RATINGS

$T_{case} = 60^{\circ}\text{C}$  unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
<b>Double Side Cooled</b>				
$I_{T(AV)}$	Mean on-state current	Half wave resistive load	840	A
$I_{T(RMS)}$	RMS value	-	1320	A
$I_r$	Continuous (direct) on-state current	-	1250	A

## SURGE RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
$I_{TSM}$	Surge (non-repetitive) on-state current	10ms half sine, $T_{case} = 125^{\circ}\text{C}$ $V_R = 0$	12.0	kA
$I^2t$	$I^2t$ for fusing		0.72	MA <sup>2</sup> s

## THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Conditions	Min.	Max.	Units	
$R_{th(j-c)}$	Thermal resistance - junction to case	Double side cooled	DC	-	22.0	$^{\circ}\text{C}/\text{kW}$
		Single side cooled	Anode DC	-	40.5	$^{\circ}\text{C}/\text{kW}$
			Cathode DC	-	50.1	$^{\circ}\text{C}/\text{kW}$
$R_{th(c-h)}$	Thermal resistance - case to heatsink	Clamping force 23kN (with mounting compound)	Double side	-	4.0	$^{\circ}\text{C}/\text{kW}$
			Single side	-	8.0	$^{\circ}\text{C}/\text{kW}$
$T_{vj}$	Virtual junction temperature	Blocking $V_{DRM} / V_{RRM}$	-	125	$^{\circ}\text{C}$	
$T_{stg}$	Storage temperature range		-55	125	$^{\circ}\text{C}$	
$F_m$	Clamping force		20	25	kN	

**DYNAMIC CHARACTERISTICS**

Symbol	Parameter	Test Conditions	Min.	Max.	Units	
<b>I<sub>RRM</sub>/I<sub>DRM</sub></b>	Peak reverse and off-state current	At V <sub>RRM</sub> /V <sub>DRM</sub> , T <sub>case</sub> = 125°C	-	200	mA	
<b>V<sub>TM</sub></b>	Instantaneous forward voltage	At 2900A peak, T <sub>j</sub> = 25°C	2.70	3.10	V	
<b>dV/dt</b>	Max. linear rate of rise of off-state voltage	To 67% V <sub>DRM</sub> , T <sub>j</sub> = 125°C, gate open	-	1500	V/μs	
<b>dI/dt</b>	Rate of rise of on-state current	From 67% V <sub>DRM</sub> to 2x I <sub>T(AV)</sub> Gate source 30V, 10Ω tr < 0.5μs, T <sub>j</sub> = 125°C	Repetitive 50Hz	-	100	A/μs
		Non-repetitive	-	200	A/μs	
<b>V<sub>T(TO)</sub></b>	Threshold voltage - Low level	300A to 1000A at T <sub>case</sub> = 125°C	-	1.01	V	
	Threshold voltage - High level	1000A to 3500A at T <sub>case</sub> = 125°C	-	1.19	V	
<b>r<sub>T</sub></b>	On-state slope resistance - Low level	300A to 1000A at T <sub>case</sub> = 125°C	-	1.13	mΩ	
	On-state slope resistance - High level	1000A to 3500A at T <sub>case</sub> = 125°C	-	0.95	mΩ	
<b>t<sub>gd</sub></b>	Delay time	V <sub>D</sub> = 67% V <sub>DRM</sub> , gate source 30V, 10Ω tr = 0.5μs, T <sub>j</sub> = 25°C	-	3	μs	
<b>t<sub>q</sub></b>	Turn-off time	T <sub>j</sub> = 125°C, I <sub>peak</sub> = 1000A, t <sub>p</sub> = 1000μs, V <sub>RM</sub> = 100V, dI/dt = 5A/μs, dV <sub>DR</sub> /dt = 20V/μs linear to 2500V	600	1000	μs	
<b>Q<sub>S</sub></b>	Stored charge	I <sub>T</sub> = 1000A, t <sub>p</sub> = 1000μs, T <sub>j</sub> = 125°C, dI/dt = 5A/μs, V <sub>R peak</sub> = 100V. [LEM]	2500	4000	μC	
<b>I<sub>RR</sub></b>	Reverse recovery current		90	120	A	
<b>Q<sub>S</sub></b>	Stored charge	T <sub>j</sub> = 125°C, dI/dt = 1A/μs, V <sub>R peak</sub> ~ 3900V, V <sub>R</sub> ~ 2450V	2150	3760	μC	
<b>I<sub>RR</sub></b>	Reverse recovery current		36	46	A	
<b>I<sub>L</sub></b>	Latching current	T <sub>j</sub> = 25°C, V <sub>D</sub> = 5V	-	3	A	
<b>I<sub>H</sub></b>	Holding current	T <sub>j</sub> = 25°C, R <sub>G-K</sub> = ∞, I <sub>TM</sub> = 500A, I <sub>T</sub> = 5A	-	300	mA	

## GATE TRIGGER CHARACTERISTICS AND RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
$V_{GT}$	Gate trigger voltage	$V_{DRM} = 5V, T_{case} = 25^{\circ}C$	1.5	V
$V_{GD}$	Gate non-trigger voltage	At 50% $V_{DRM}, T_{case} = 125^{\circ}C$	0.4	V
$I_{GT}$	Gate trigger current	$V_{DRM} = 5V, T_{case} = 25^{\circ}C$	350	mA
$I_{GD}$	Gate non-trigger current	At 50% $V_{DRM}, T_{case} = 125^{\circ}C$	15	mA

## CURVES

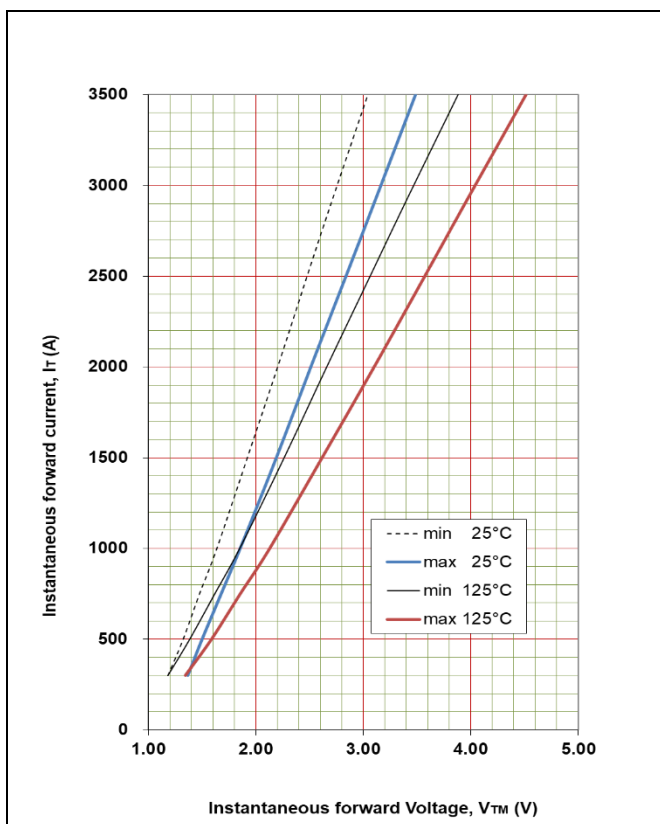


Fig. 2 Maximum & minimum on state characteristics

## $V_{TM}$ EQUATION

$$V_{TM} = A + B \cdot \ln(I_T) + C \cdot I_T + D \cdot \sqrt{I_T}$$

Where  $A = 0.265719$

$B = 0.057113$

$C = 0.000832$

$D = 0.008411$

These values are valid for  $T_j = 125^{\circ}C$  for  $I_T$  300A to 3500A

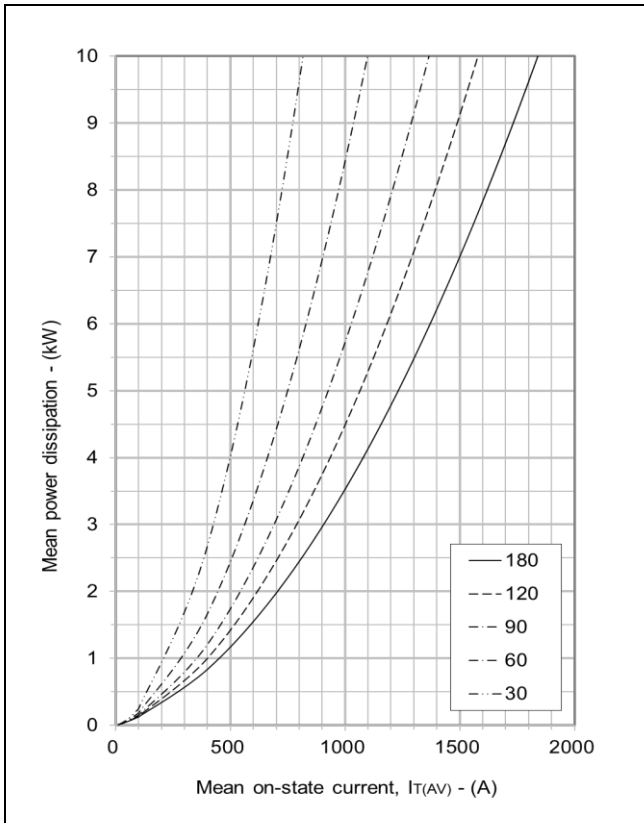


Fig. 3 On-state power dissipation - sine wave

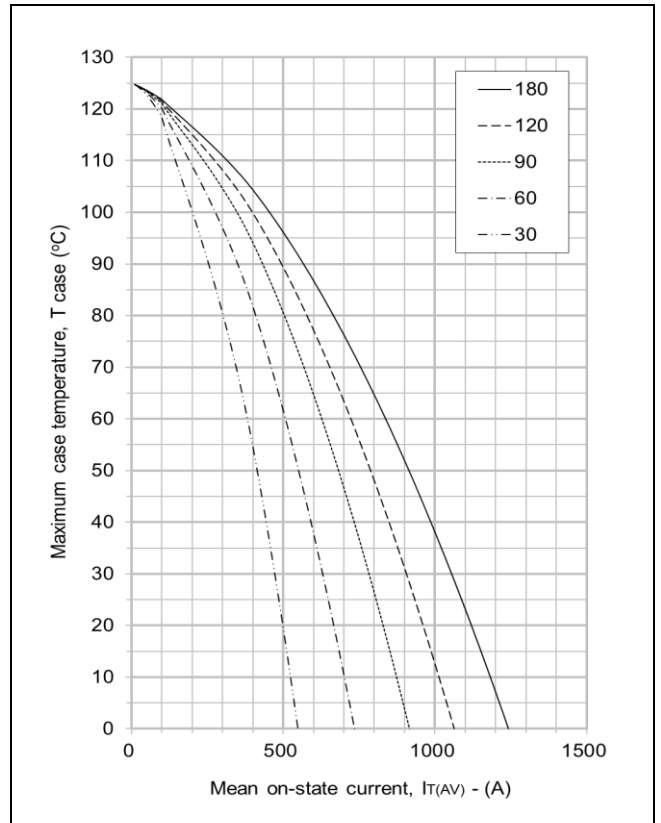


Fig. 4 Maximum permissible case temperature, double side cooled - sine wave

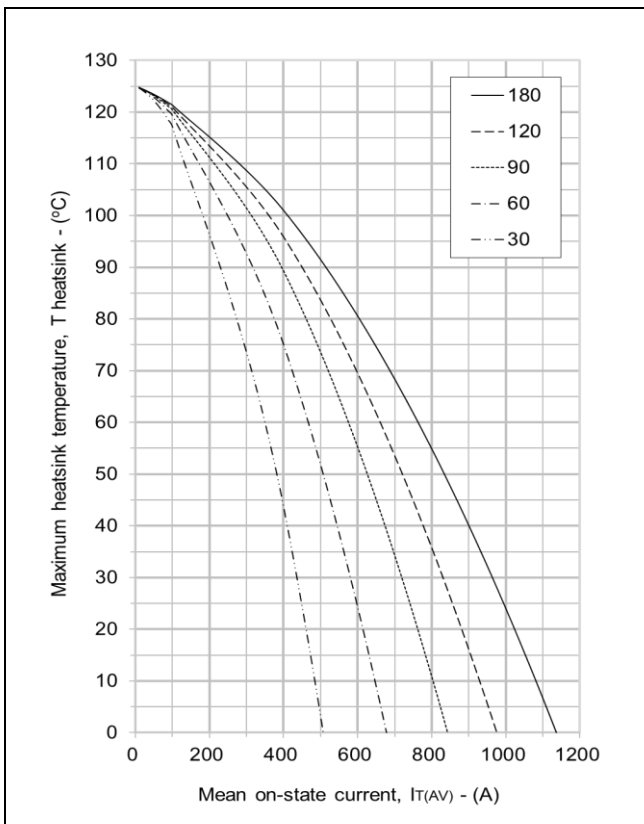


Fig. 5 Maximum permissible heatsink temperature, double side cooled - sine wave

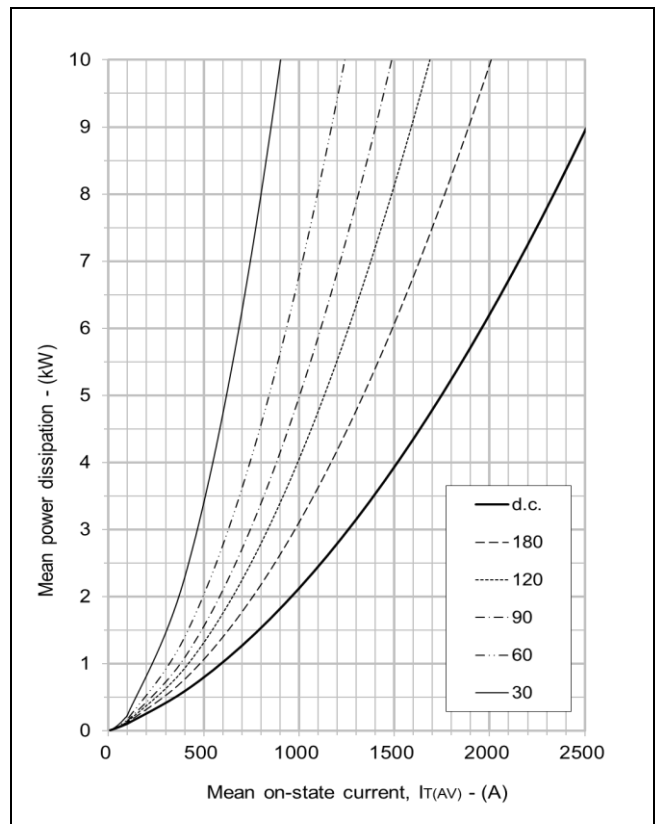


Fig. 6 On-state power dissipation - rectangular wave

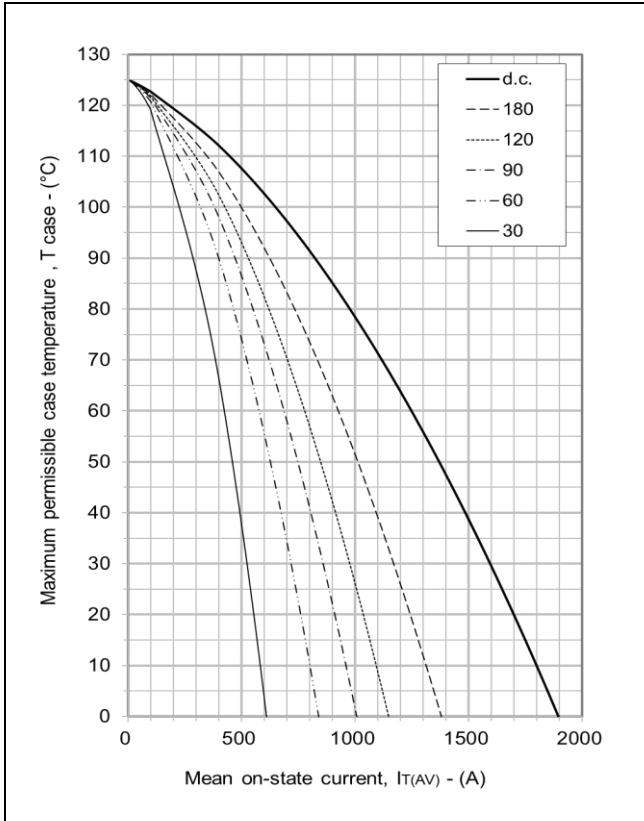


Fig. 7 Maximum permissible case temperature, double side cooled - rectangular wave

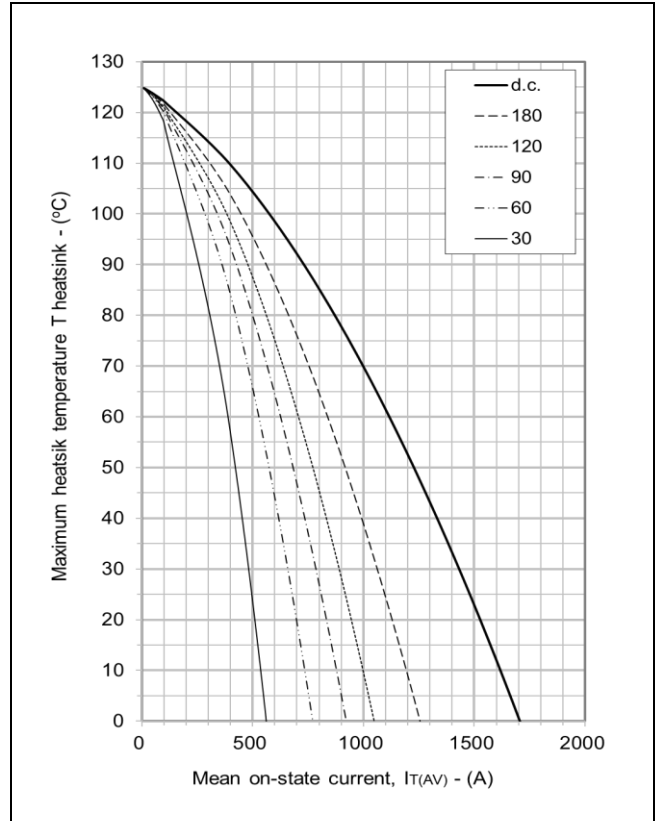


Fig. 8 Maximum permissible heatsink temperature, double side cooled - rectangular wave

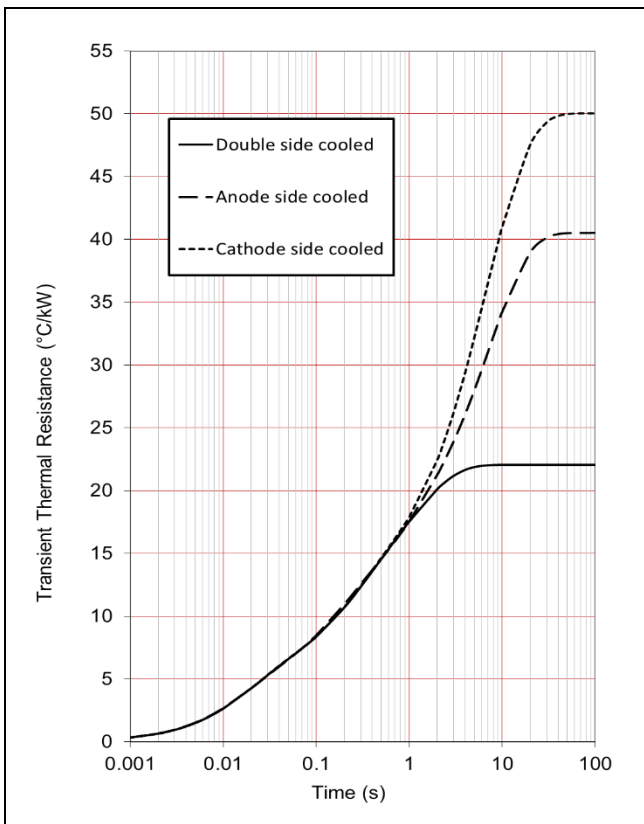


Fig. 9 Maximum (limit) transient thermal impedance – junction to case (degC/kW)

		1	2	3	4
Double side cooled	Ri(°C/kW)	3.473	4.905	9.146	4.522
	Ti(s)	0.146	0.017	1.283	0.377
Anode side cooled	Ri(°C/kW)	6.039	4.278	5.130	25.087
	Ti(s)	0.136	0.014	0.659	7.236
Cathode side cooled	Ri(°C/kW)	7.667	5.053	9.736	27.599
	Ti(s)	0.224	0.017	4.057	8.278

$$Z_{th} = \sum_{i=1}^{i=4} R_i \cdot \left(1 - \exp\left(-\frac{T}{T_i}\right)\right)$$

$\Delta R_{th(j-c)}$  Conduction

Tables show the increments of thermal resistance  $R_{th(j-c)}$  when the device operates at conduction angles other than d.c.

Double side cooling			Anode Side Cooling			Cathode Sided Cooling		
$\theta^*$	$\Delta Z_{th}(z)$		$\theta^*$	$\Delta Z_{th}(z)$		$\theta^*$	$\Delta Z_{th}(z)$	
	sine.	rect.		sine.	rect.		sine.	rect.
180	3.03	2.07	180	3.03	2.07	180	3.12	2.12
120	3.49	2.95	120	3.49	2.95	120	3.61	3.04
90	3.99	3.43	90	3.99	3.43	90	4.13	3.54
60	4.43	3.94	60	4.43	3.94	60	4.60	4.08
30	4.77	4.49	30	4.76	4.48	30	4.96	4.66
15	4.92	4.77	15	4.92	4.77	15	5.13	4.97

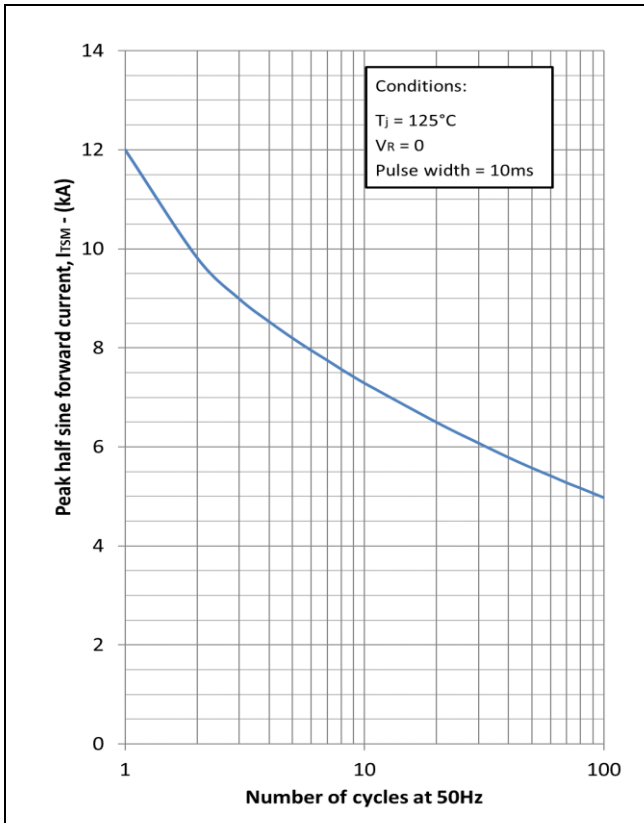


Fig. 10 Multi-cycle surge current

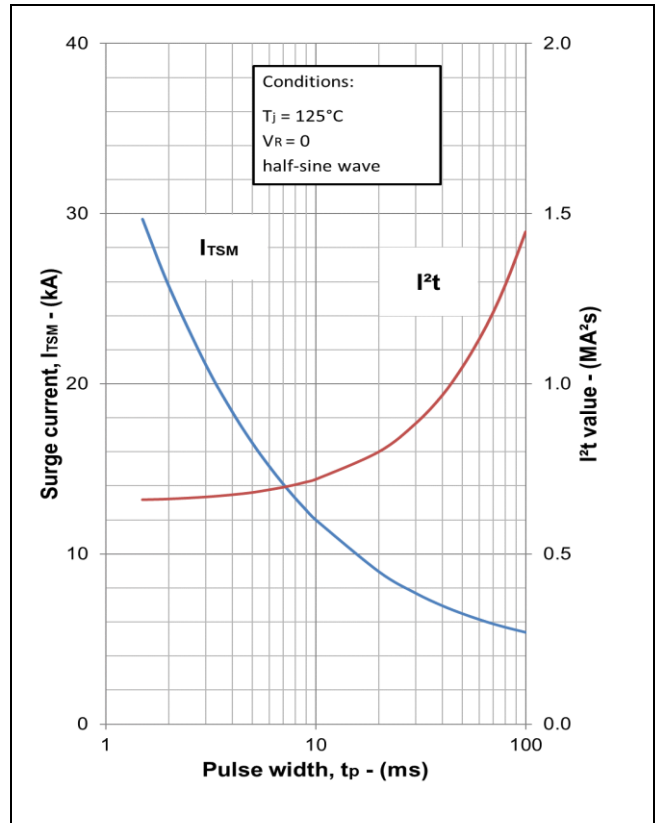


Fig. 11 Single-cycle surge current

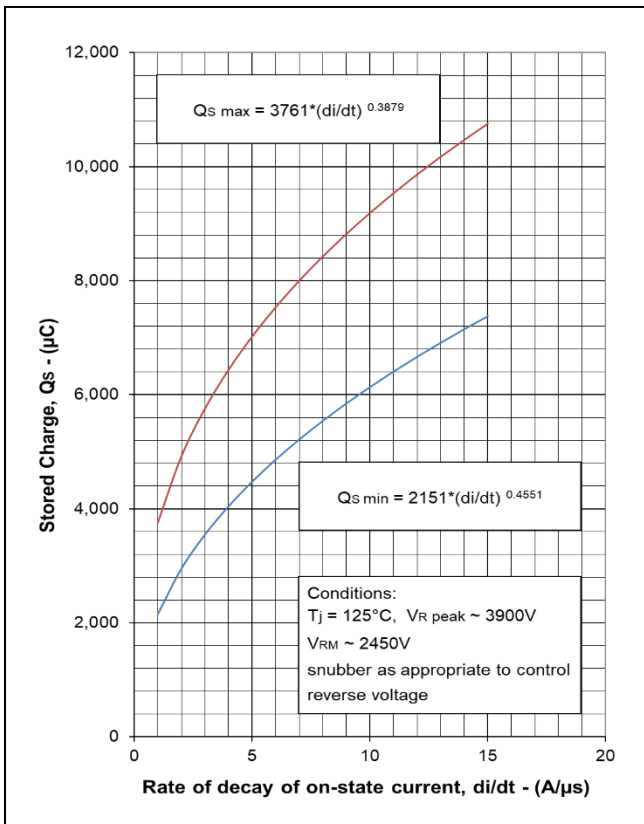


Fig. 12 Stored charge

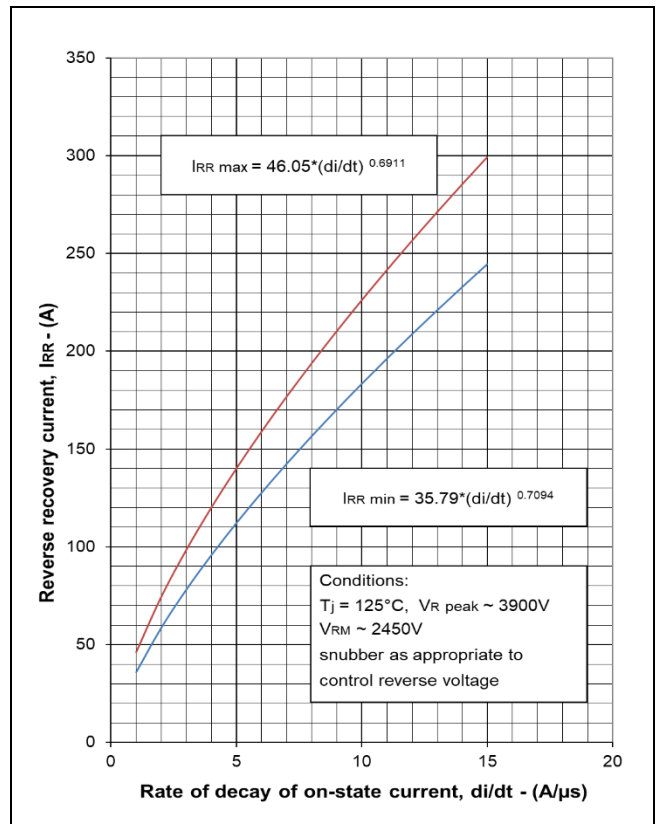


Fig. 13 Reverse recovery current

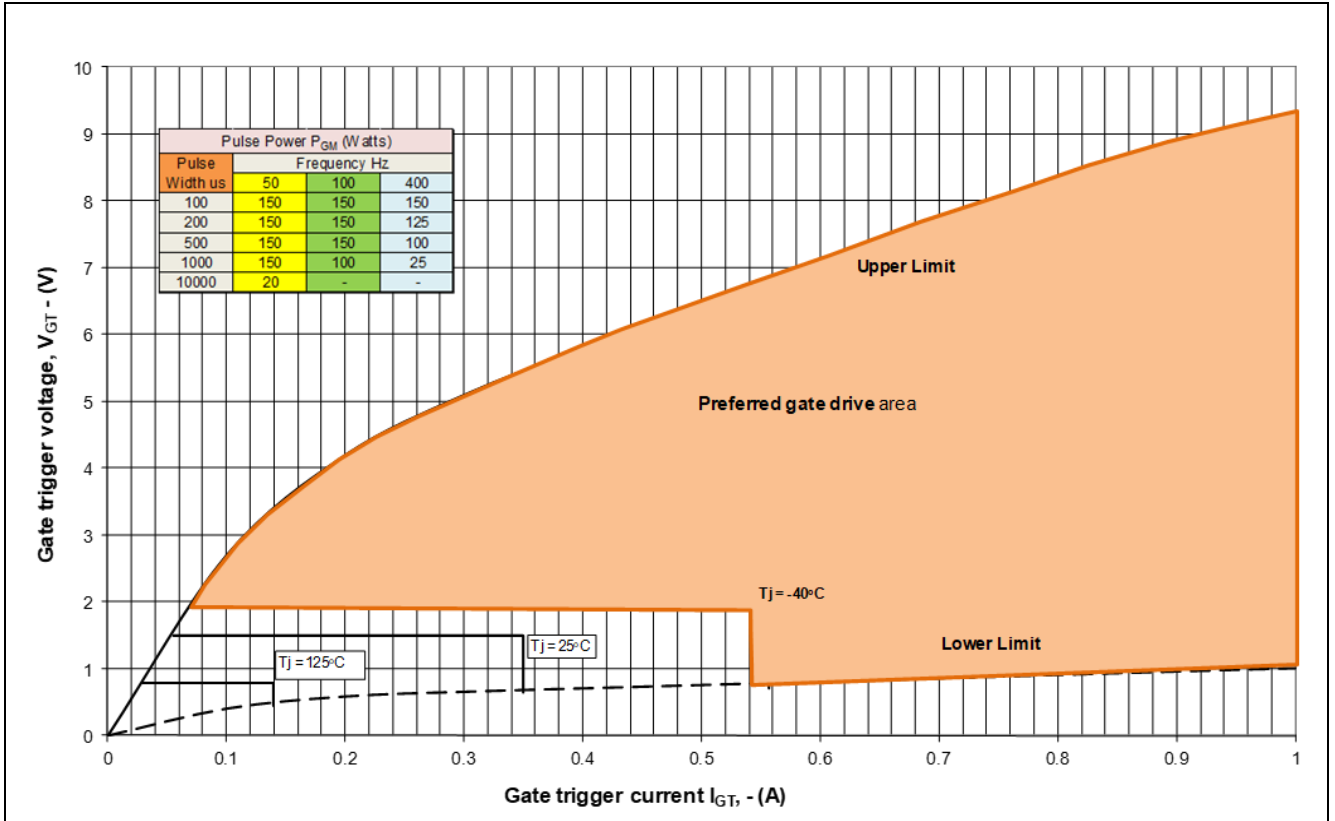


Fig. 14 Gate characteristics

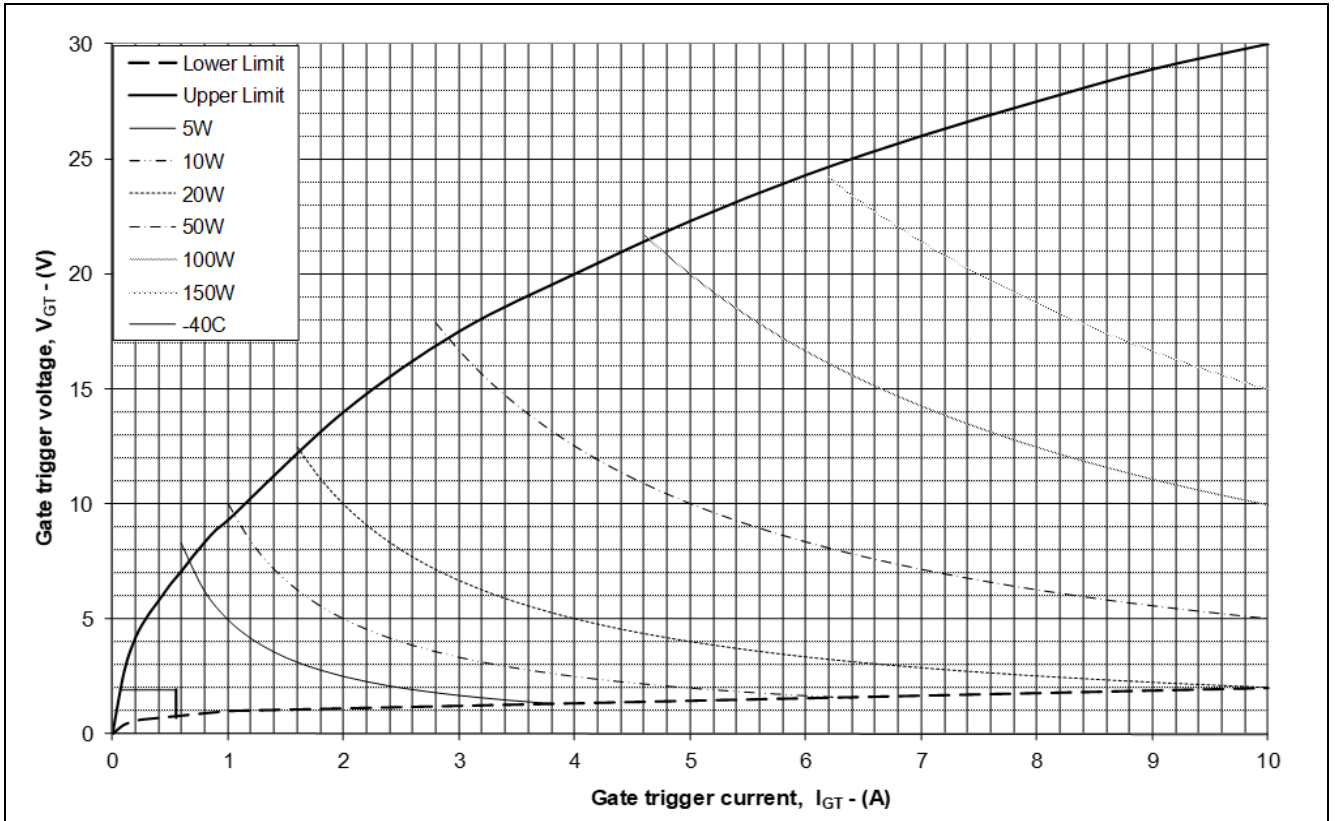


Fig. 15 Gate characteristics



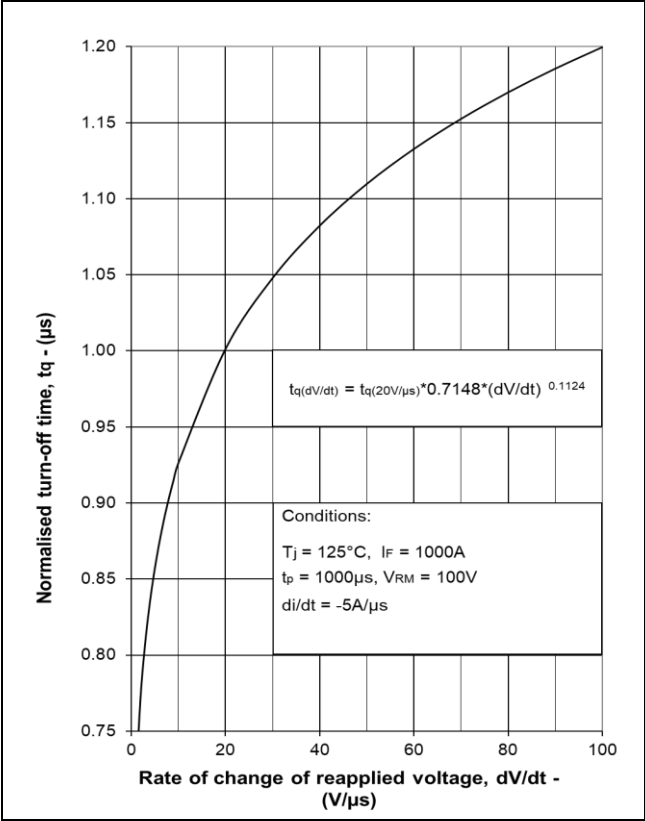


Fig. 16 Turn-off time

**PACKAGE DETAILS**

For further package information, please contact Customer services.

All dimensions in mm, unless stated otherwise.

DO NOT SCALE

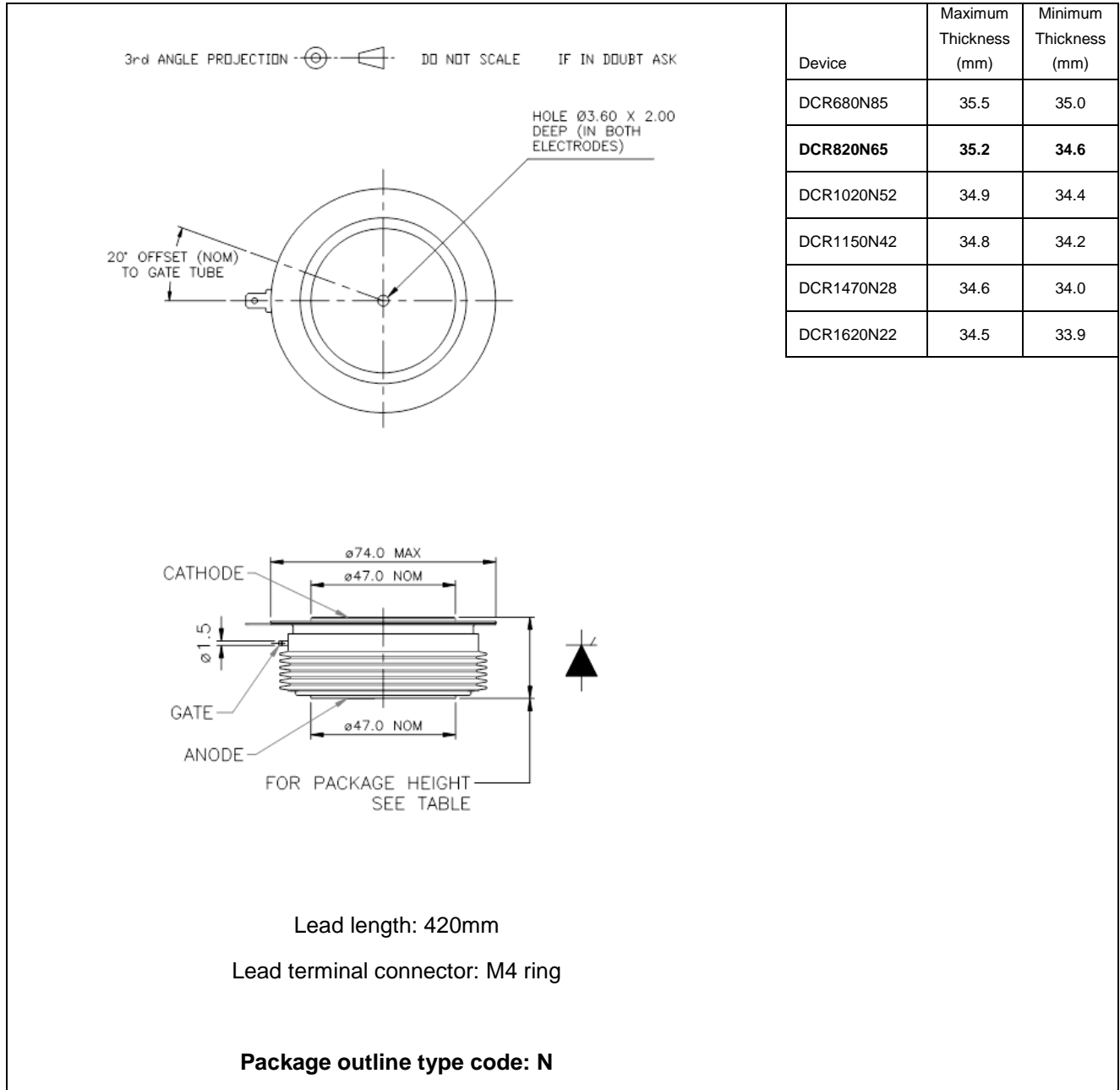


Fig. 17 Package outline

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