DG858BW45

Gate Turn-off Thyristor

Replaces DS4906-4 DS4906-5 July 2014 (LN31733)

FEATURES

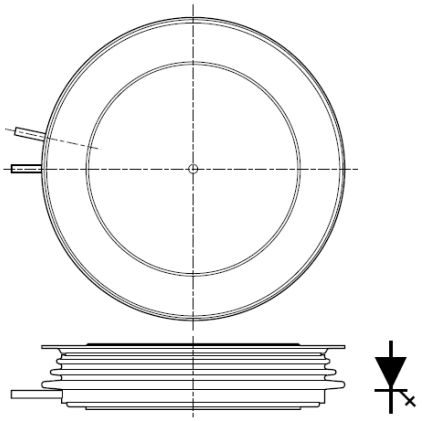
- Double Side Cooling
- High Reliability In Service
- High Voltage Capability
- Fault Protection Without Fuses
- High Surge Current Capability
- Turn-off Capability Allows Reduction in Equipment Size and Weight. Low Noise Emission Reduces Acoustic Cladding Necessary For Environmental Requirements

APPLICATIONS

- Variable speed AC motor drive inverters (VSD-AC) including Traction drives
- Uninterruptable Power Supplies
- High Voltage Converters
- Choppers
- Welding
- Induction Heating
- DC/DC Converters

KEY PARAMETERS

I_{TCM}	**3000A**
V_{DRM}	**4500V**
$I_{(AV)}$	**1180A**
dV_D/dt*	**1000V/µs**
dI_T/dt	**400A/µs**



Package outline type code: W.
See Package Details for further information.

Fig. 1 Package outline

VOLTAGE RATINGS

Type Number	Repetitive Peak Off-state Voltage V_{DRM} (V)	Repetitive Peak Reverse Voltage V_{RRM} (V)	Conditions
DG858BW45	4500	16	T_{vj} = 125°C, I_{DM} =100mA, I_{RRM} = 50mA

CURRENT RATINGS

Symbol	Parameter	Conditions	Max.	Units
I_{TCM}	Repetitive peak controllable on-state current	V_D = 66%V_{DRM}, T_j = 125°C, dI_{GQ}/dt = 40A/µs, C_S = 3µF	3000	A
$I_{T(AV)}$	Mean on-state current	T_{HS} = 80°C, Double side cooled. Half sine 50Hz	1180	A
$I_{T(RMS)}$	RMS on-state current	T_{HS} = 80°C, Double side cooled. Half sine 50Hz	1850	A

SURGE RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
I_{TSM}	Surge (non repetitive) on-state current	10ms half sine. T_j = 125°C	20.0	kA
I^2t	I^2t for fusing	10ms half sine. T_j = 125°C	2.0	MA^2s
di_T/dt	Critical rate of rise of on-state current	V_D = 3000V, I_T = 3000A, T_j = 125°C, I_{FG} > 40A, Rise time > 1.0 μs	300	A/μs
dV_D/dt	Rate of rise of off-state voltage	To 66% V_{DRM}; R_{GK} ≤ 1.5Ω, T_j = 125°C	130	V/μs
		To 66% V_{DRM}; V_{RG} ≤ -2V, T_j = 125°C	1000	V/μs
L_S	Peak stray inductance in snubber circuit	I_T = 3000A, V_D = V_{DRM}, Tj = 125°C, dI_{GQ} = 40A/us, C_S =3.0uF	200	nH

GATE RATINGS

Symbol	Parameter	Test Conditions	Min.	Max.	Units
V_{RGM}	Peak reverse gate voltage	This value may exceeded during turn-off	-	16	V
I_{FGM}	Peak forward gate current		-	100	A
$P_{FG(AV)}$	Average forward gate power		-	20	W
P_{RGM}	Peak reverse gate power		-	24	kW
di_{GQ}/dt	Rate of rise of reverse gate current		20	60	A/μs
$t_{ON(min)}$	Minimum permissible on time		50	-	μs
$t_{OFF(min)}$	Minimum permissible off time		100	-	μs

THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Conditions		Min.	Max.	Units
$R_{th(j-hs)}$	Thermal resistance – junction to heatsink surface	Double side cooled	DC	-	0.011	°C/W
		Single side cooled	Anode DC	-	0.017	°C/W
			Cathode DC	-	0.033	°C/W
$R_{th(c-hs)}$	Contact thermal resistance	Clamping force 36.0kN With mounting compound	Per contact	-	0.0021	°C/W
T_{vj}	Virtual junction temperature	On-state (conducting)		-40	125	°C
T_{op}/T_{stg}	Operating junction/storage temperature range			-40	125	°C
F_m	Clamping force			36.0	44.0	kN

CHARACTERISTICS

Tj =125°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min.	Max.	Units
$V_{TM)}$	On-state voltage	At 4000A peak, $I_{G(ON)}$ = 10A d.c.	-	4.0	V
I_{DM}	Peak off-state current	V_{DRM} = 4500V, V_{RG} = 0V	-	100	mA
I_{RRM}	Peak reverse current	V_{RRM} = 16V	-	50	mA
V_{GT}	Gate trigger voltage	V_D = 24V, I_T = 100A, Tj = 25°C	-	1.2	V
I_{GT}	Gate trigger current	V_D = 24V, I_T = 100A, Tj = 25°C	-	4.0	A
I_{RGM}	Reverse gate cathode current	V_{RGM} = 16V, No gate/cathode resistor	-	50	mA
E_{ON}	Turn-on Energy	V_D = 2000V	-	2700	mJ
t_d	Delay time	I_T = 3000A, dI_T/dt = 300A/µs	-	2.0	µs
t_r	Rise time	I_{FG} = 40A, rise time < 1.0µs	-	6.0	µs
E_{OFF}	Turn-off energy		-	13500	mJ
t_{gs}	Storage time		-	25	µs
t_{gf}	Fall time	I_T = 3000A, V_{DM} = VDRM		2.5	µs
t_{gq}	Gate controlled turn-off time	Snubber Cap Cs = 3.0µC	-	27.5	µs
Q_{GQ}	Turn-off gate charge	di_{GQ}/dt = 40A/us		12000	µC
Q_{GQT}	Total turn-off gate charge			24000	µC
I_{GQM}	Peak reverse gate current		-	950	A

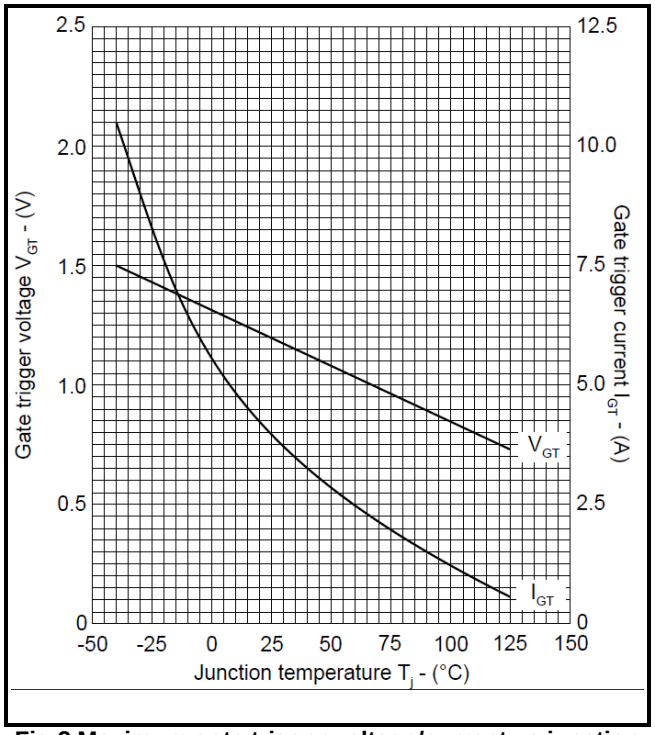


Fig.2 Maximum gate trigger voltage/current vs junction temperature

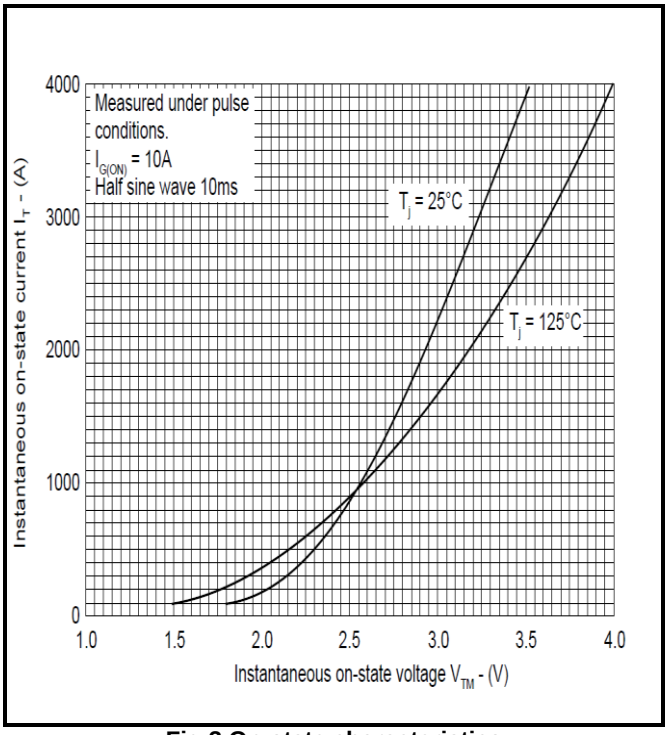


Fig.3 On-state characteristics

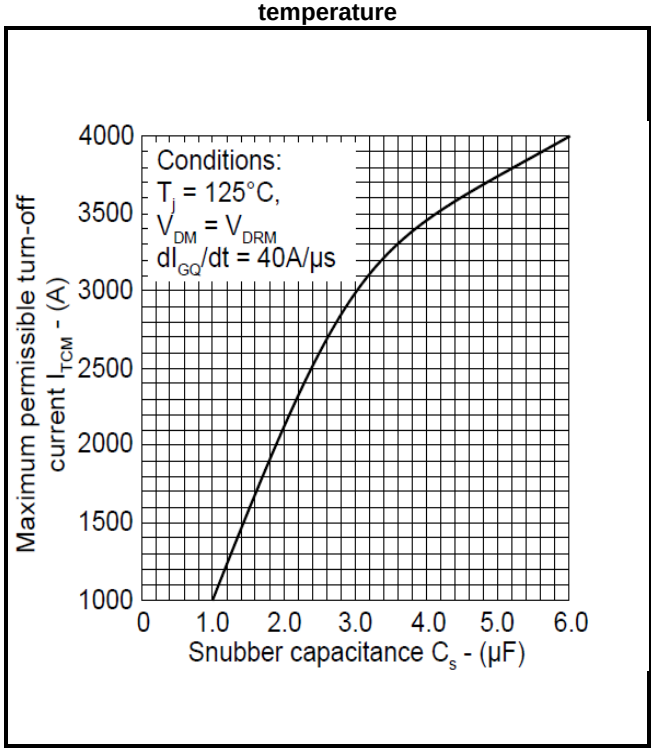


Fig.4 Maximum dependence of I_{TCM} on C_S

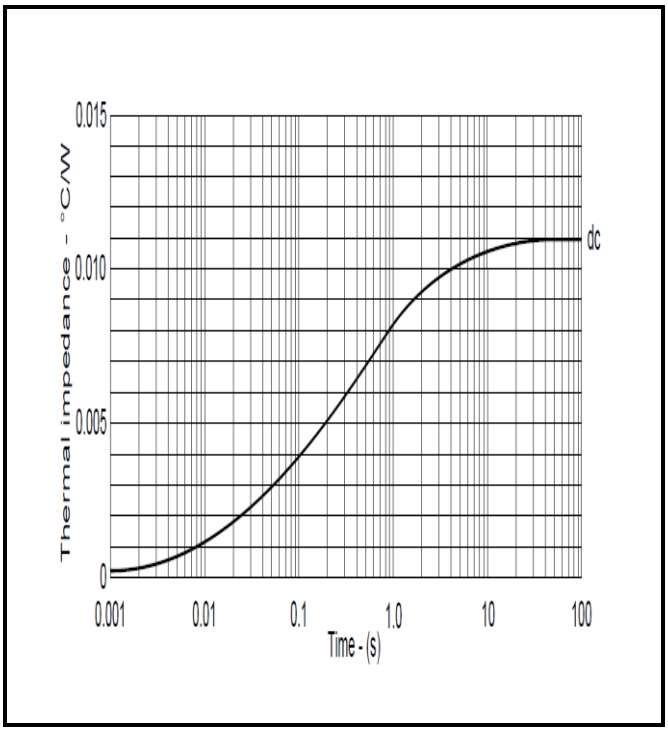


Fig.5 Maximum (limit) transient thermal impedance-double side cooled

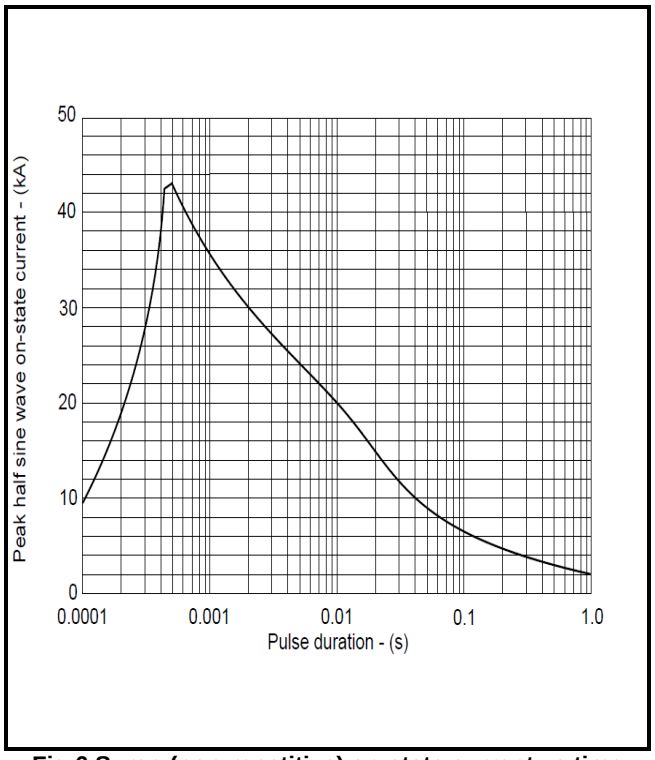


Fig.6 Surge (non-repetitive) on-state current vs time

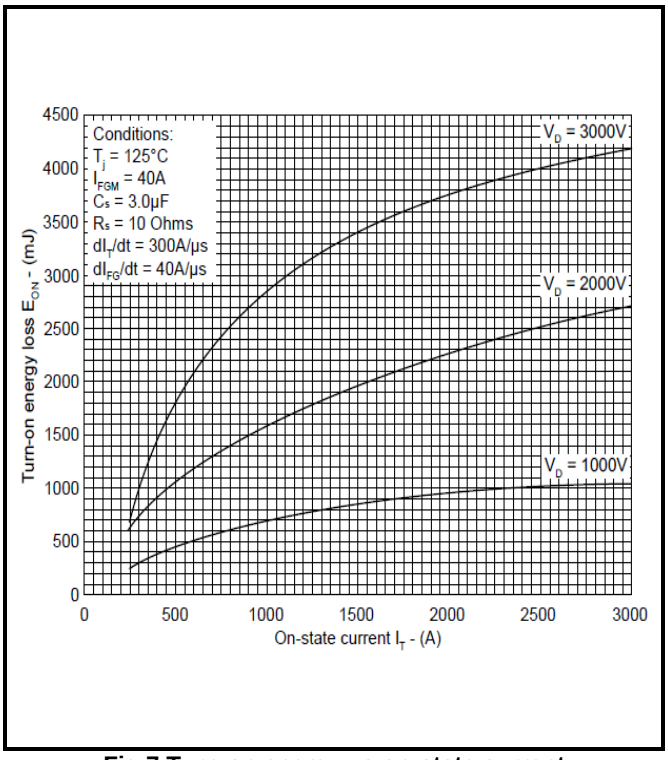


Fig.7 Turn-on energy vs on-state current

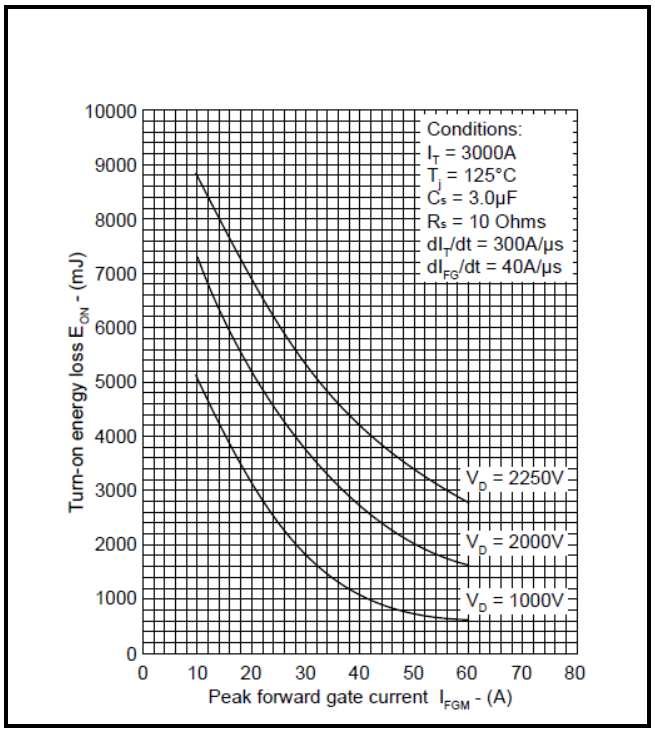


Fig.8 Turn-on energy vs forward gate current

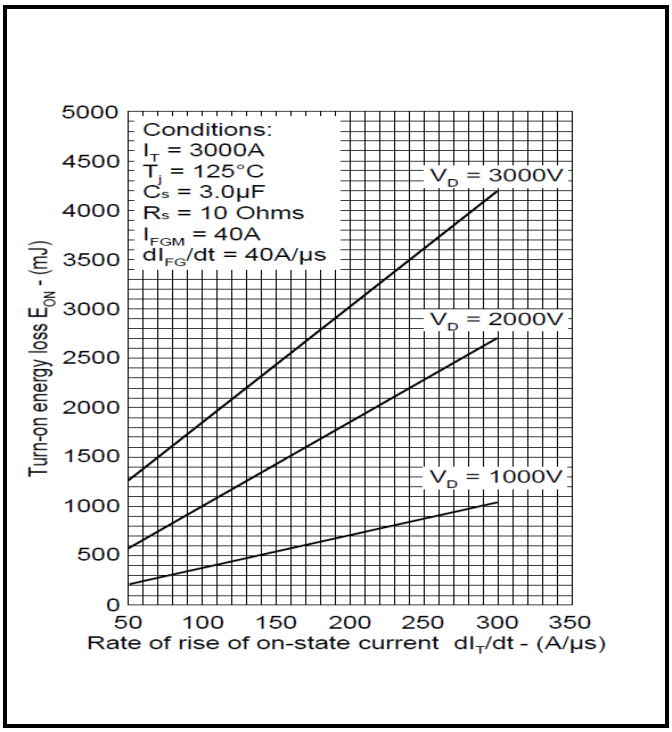


Fig.9 Turn-on energy vs rate of rise of on-state current

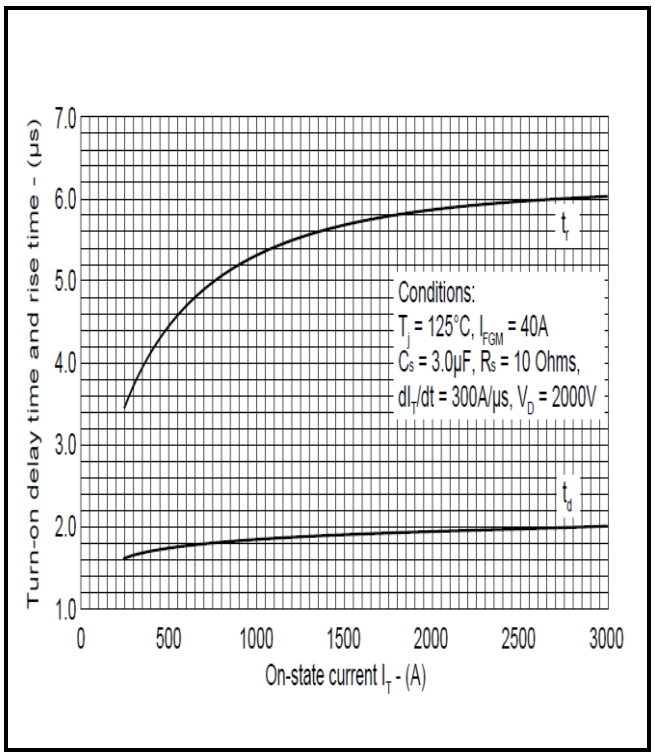


Fig.10 Delay and rise time vs on-state current

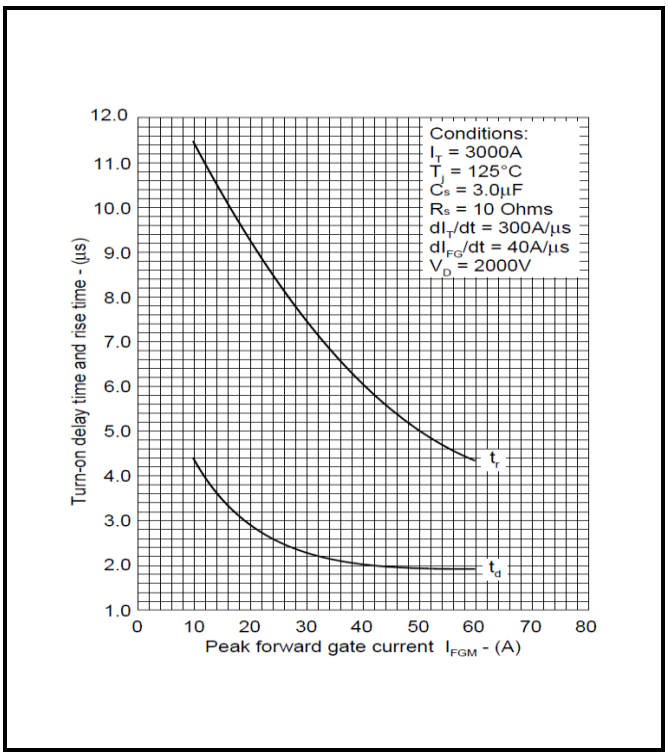


Fig.11 Delay and rise time vs peak forward gate current

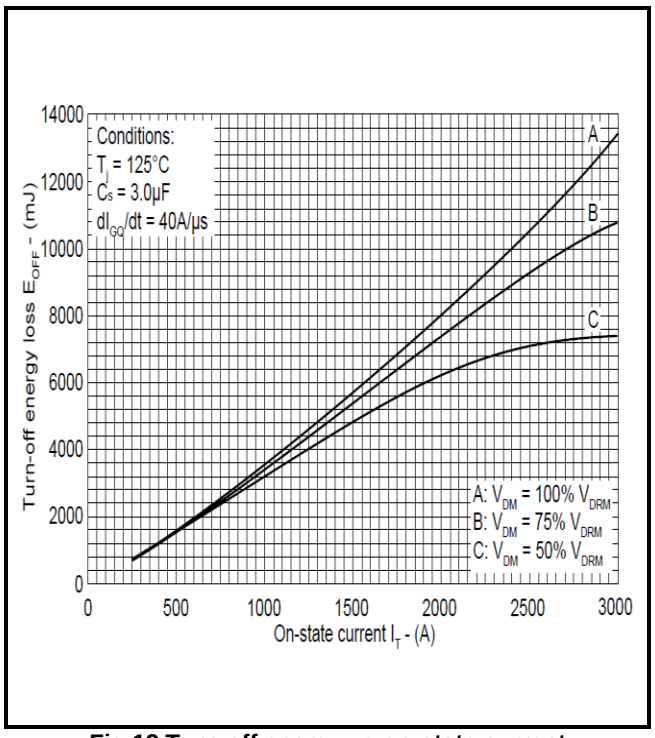


Fig.12 Turn-off energy vs on-state current

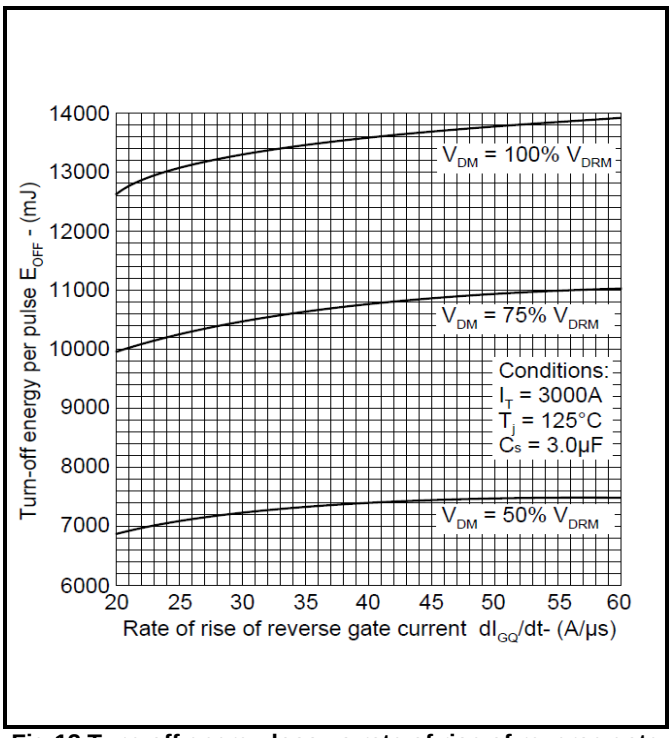


Fig.13 Turn-off energy loss vs rate of rise of reverse gate current

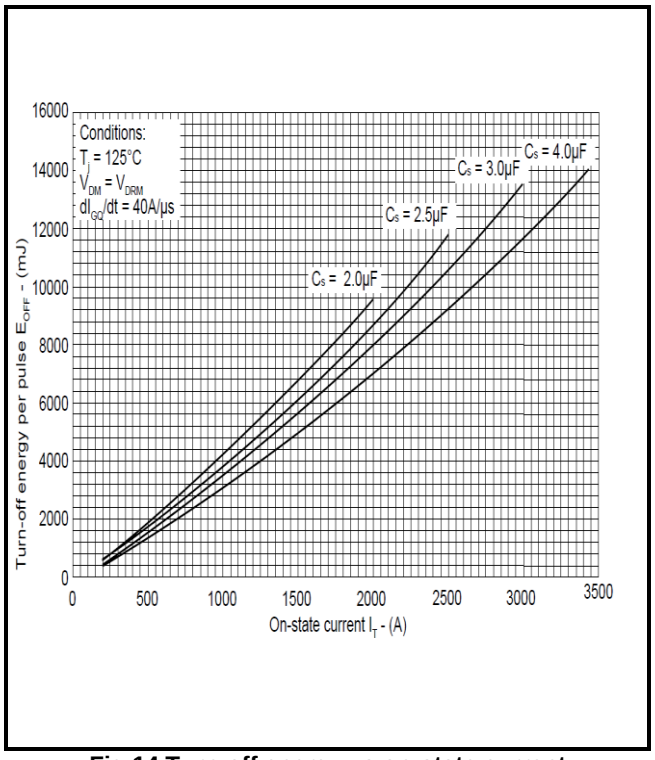


Fig.14 Turn-off energy vs on-state current

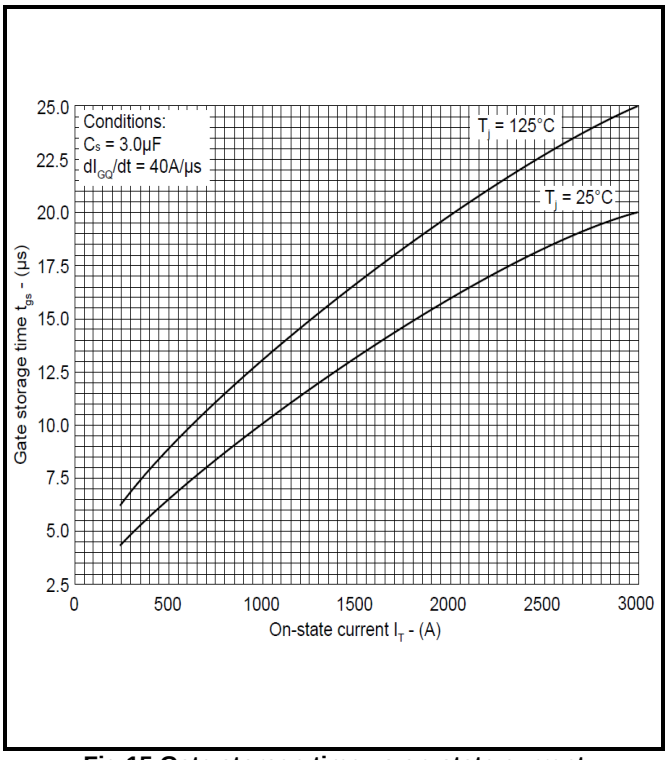


Fig.15 Gate storage time vs on-state current

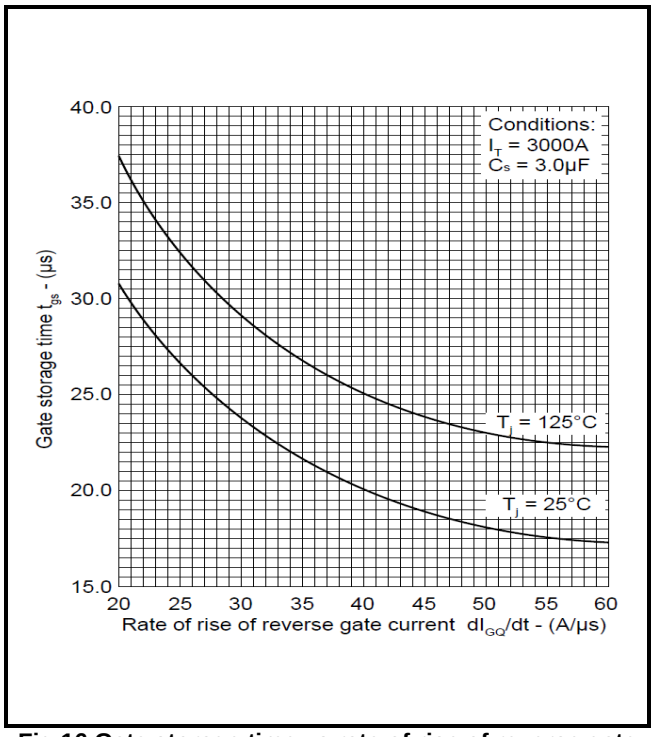


Fig.16 Gate storage time vs rate of rise of reverse gate current

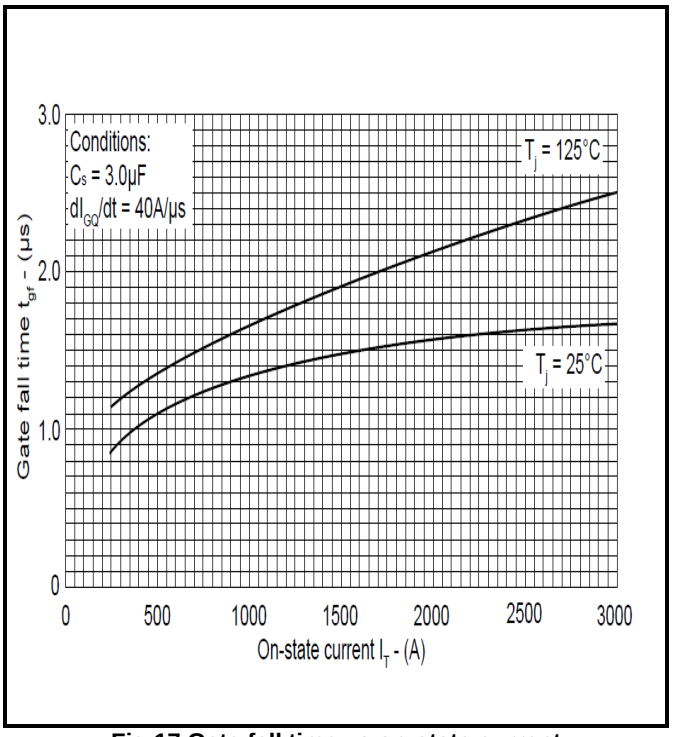


Fig.17 Gate fall time vs on-state current

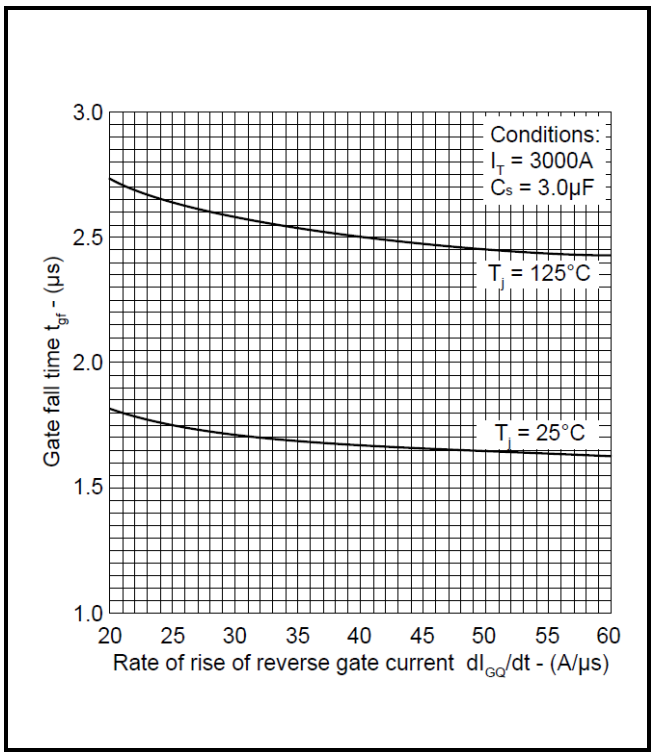


Fig.18 Gate fall time vs rate of rise of reverse gate current

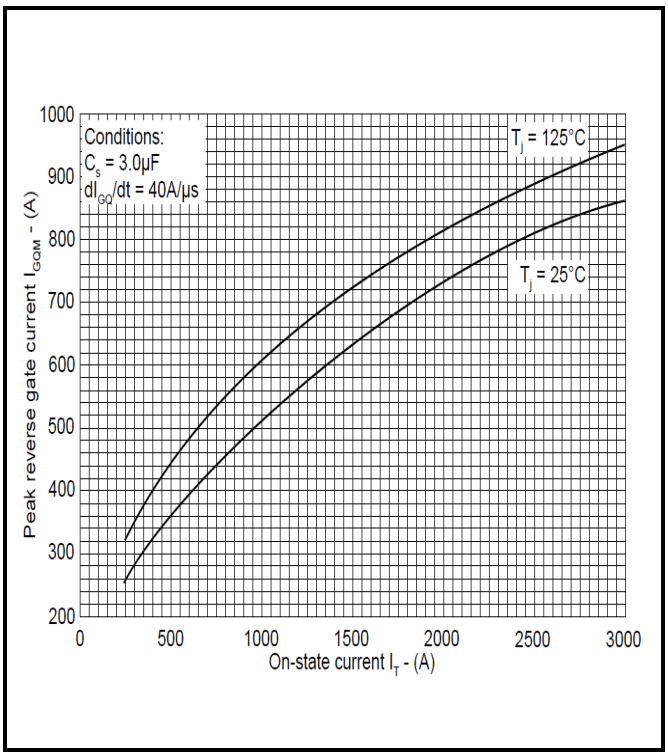


Fig.19 Peak reverse gate current vs on-state current

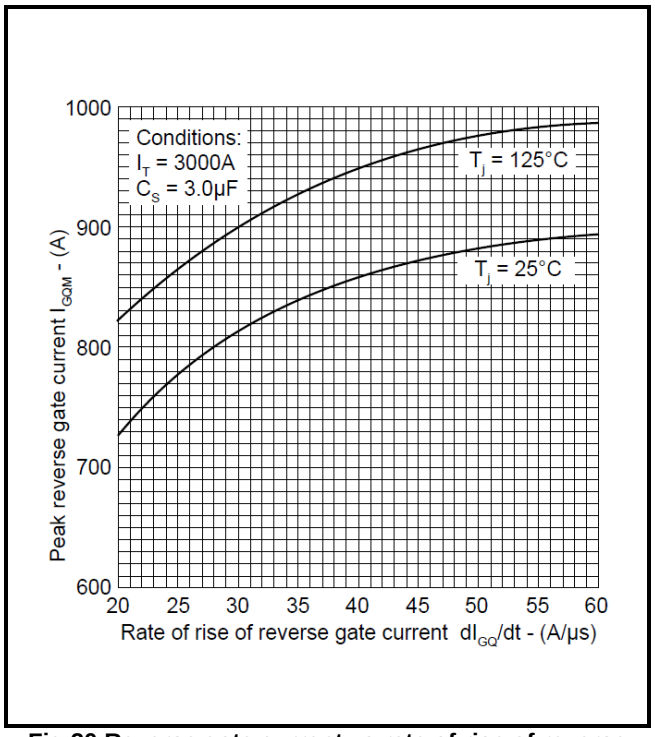


Fig.20 Reverse gate current vs rate of rise of reverse gate current

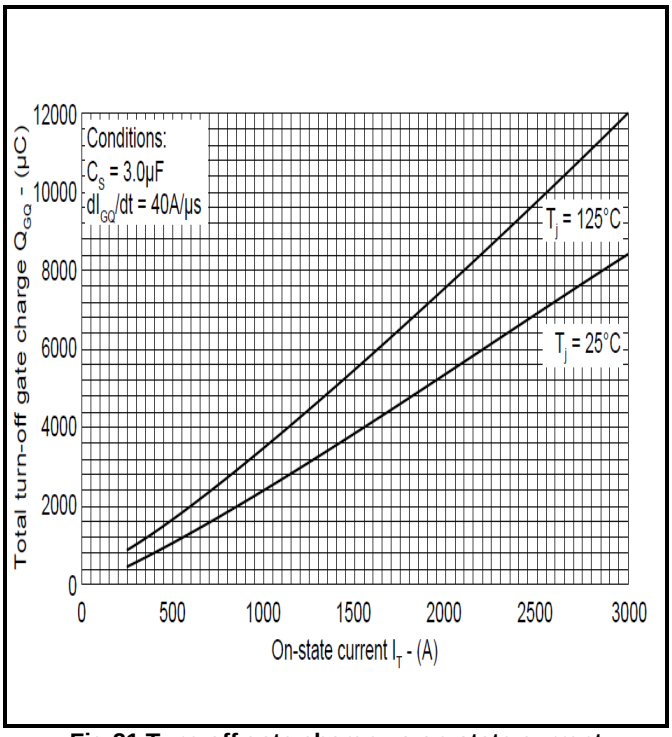


Fig.21 Turn-off gate charge vs on-state current

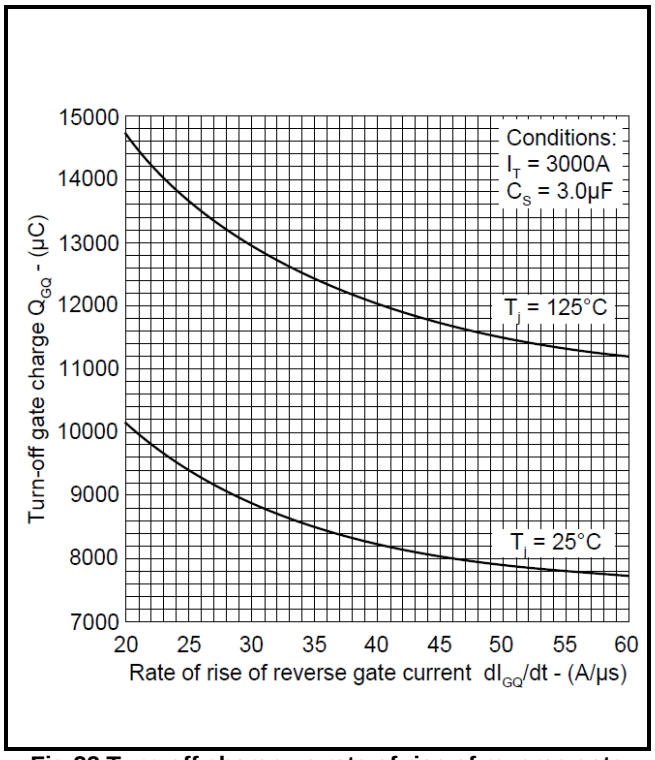


Fig.22 Turn-off charge vs rate of rise of reverse gate current

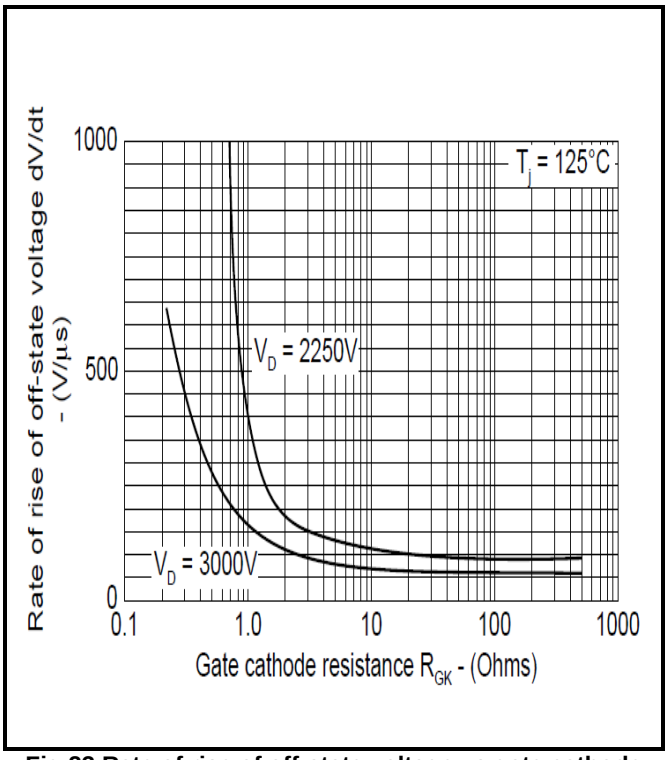
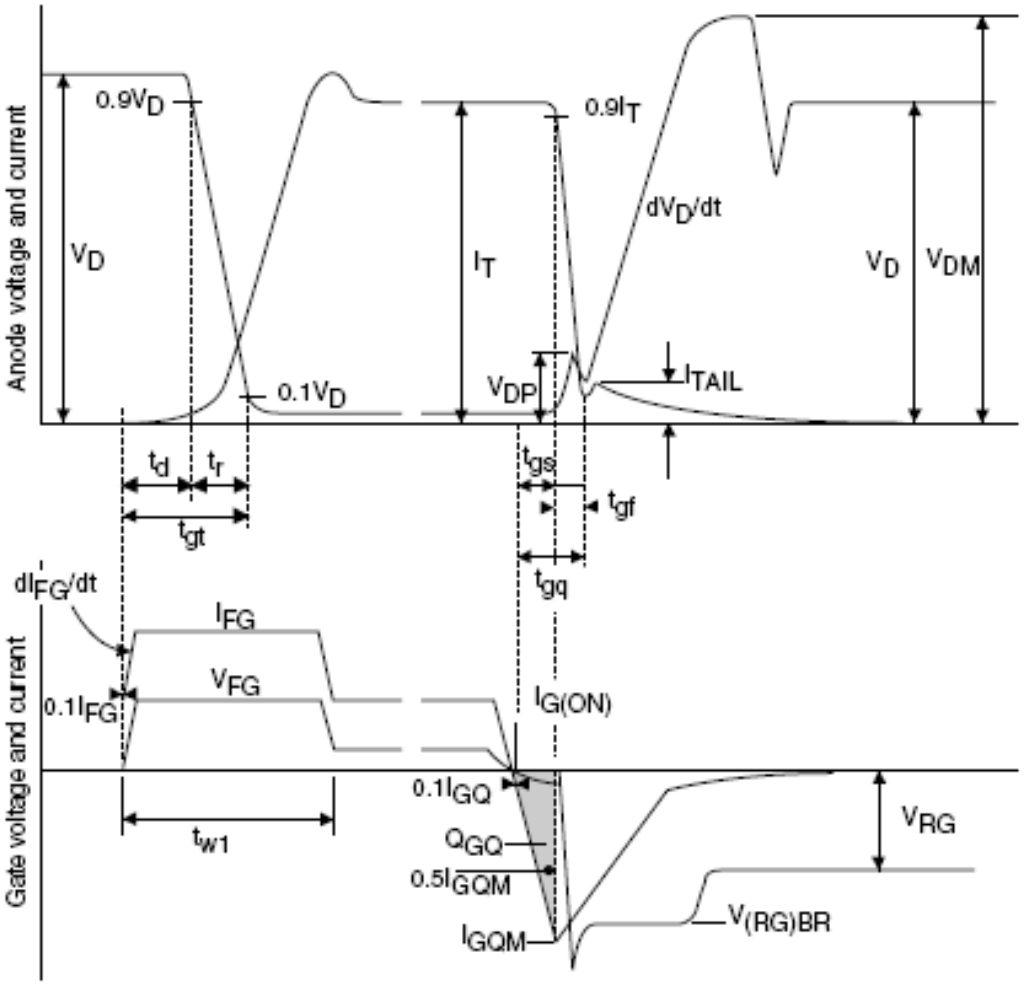


Fig.23 Rate of rise of off-state voltage vs gate cathode resistance



Recommended gate conditions:
I_{TCM} = 1000A
I_{FG} = 30A
$I_{G(ON)}$ = 4A d.c.
$t_{w1(min)}$ = 10µs
I_{GQM} = 420A
di_{GQ}/dt = 30A/µs
Q_{GQ} = 3000µC
$V_{RG(min)}$ = 2V
$V_{RG(max)}$ = 16V

These are recommended Dynex Semiconductor conditions. Other conditions are permitted according to users gate drive specifications.

Fig.24 General switching waveforms

PACKAGE DETAILS

For further package information, please contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.

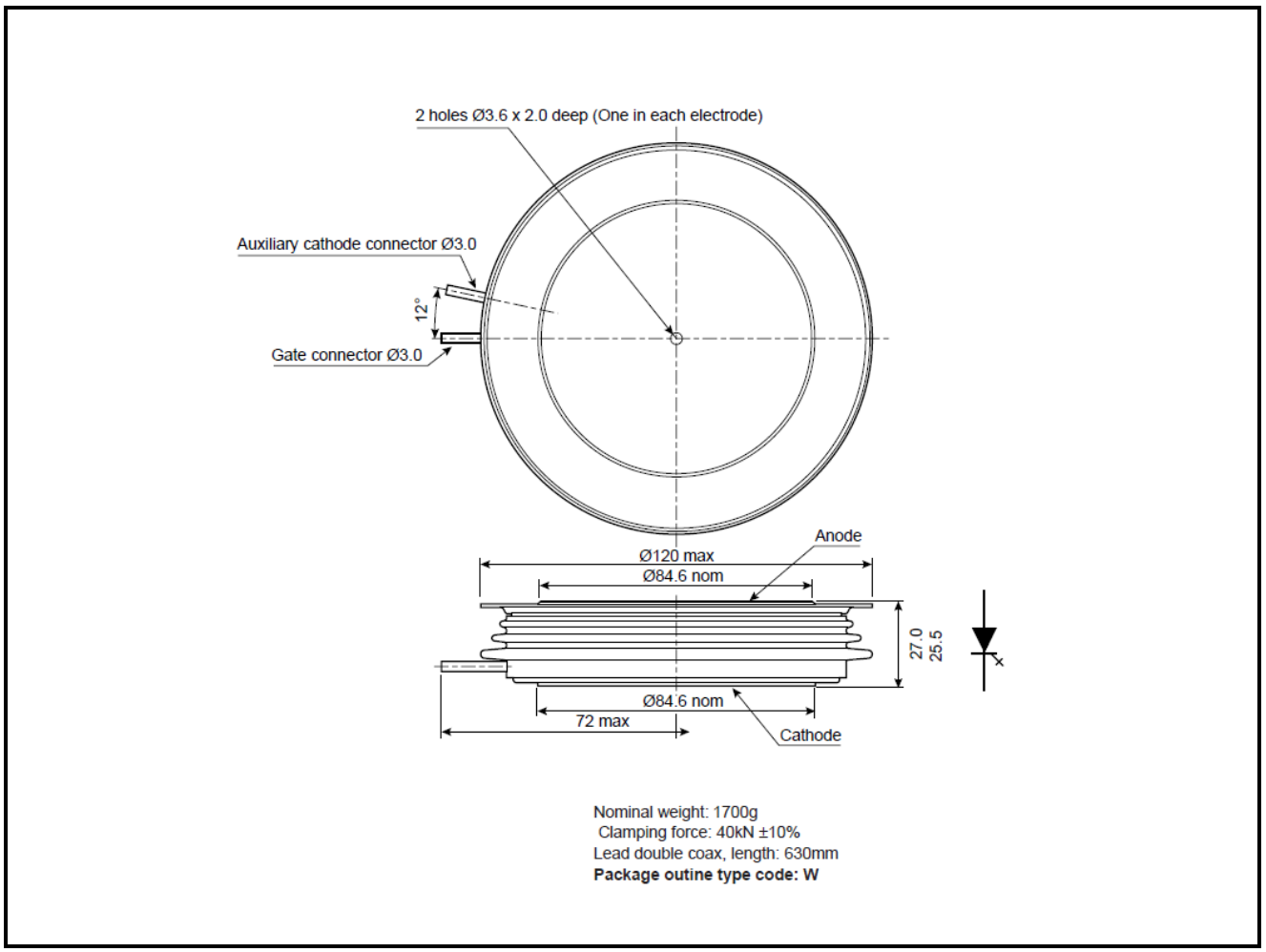


Fig.31 Package outline

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