

DRD2030F22

Rectifier Diode

DS6000-1 March 2011 (LN28186)

FEATURES

- Double Side Cooling
- High Surge Capability

KEY PARAMETERS

V _{RRM}	2200V
I _{F(AV)}	2030A
I _{FSM}	25700A

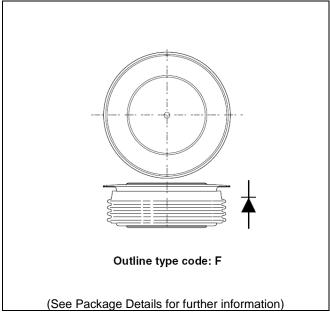


Fig. 1 Package outline

VOLTAGE RATINGS

Part and Ordering Number	Repetitive Peak Voltages V _{RRM} V	Conditions
DRD2030F22 DRD2030F20 DRD2030F18 DRD2030F16	2200 2000 1800 1600	V _{RSM} = V _{RRM} +100V

ORDERING INFORMATION

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

DRD2030F22 for a 2200V device

CURRENT RATINGS

 $T_{case} = 75^{\circ}C$ unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
Double Si	de Cooled			
I _{F(AV)}	Mean forward current	Half wave resistive load	2460	А
I _{F(RMS)}	RMS value	-	3860	А
I _F	Continuous (direct) on-state current	-	3480	А

T_{case} = 100°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
Double Si	de Cooled			
I _{F(AV)}	Mean forward current	Half wave resistive load	2030	А
I _{F(RMS)}	RMS value	-	3190	А
I _F	Continuous (direct) on-state current	-	2870	А

SURGE RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
I _{FSM}	Surge (non-repetitive) on-state current	10ms half sine, $T_{case} = 175^{\circ}C$	25.7	kA
l ² t	I ² t for fusing	$V_R = 0$	3.30	MA ² s

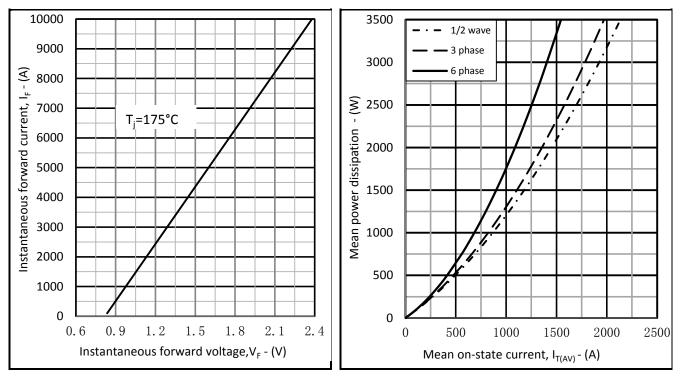
THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Condition	6	Min.	Max.	Units
R _{th(j-c)}	Thermal resistance – junction to case	Double side cooled	DC	-	0.02	°C/W
R _{th(c-h)}	Thermal resistance – case to heatsink	Double side cooled	DC	-	0.005	°C/W
T _{vj}	Virtual junction temperature	Blocking V _{DRM} / _{VRRM}		-40	175	°C
T _{stg}	Storage temperature range			-40	175	°C
F _m	Clamping force			18	26	kN

CHARACTERISTICS

Symbol	Parameter	Test Conditions	Min.	Max.	Units
V _{FM}	Forward voltage	At 1500A peak, T _{case} = 25°C	-	1.20	V
I _{RM}	Peak reverse current	At V _{DRM,} T _{case} = 175°C	-	150	mA
Qs	Total stored charge	I _F = 2000A, dI _{RR} /dt =10A/μs T _{case} = 175°C, V _R =100V	-	4000	μC
V _{TO}	Threshold voltage	At T _{vj} = 175°C	-	0.82	V
r _T	Slope resistance	At T _{vj} = 175°C	-	0.156	mΩ

CURVES



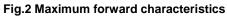


Fig.3 Dissipation curves

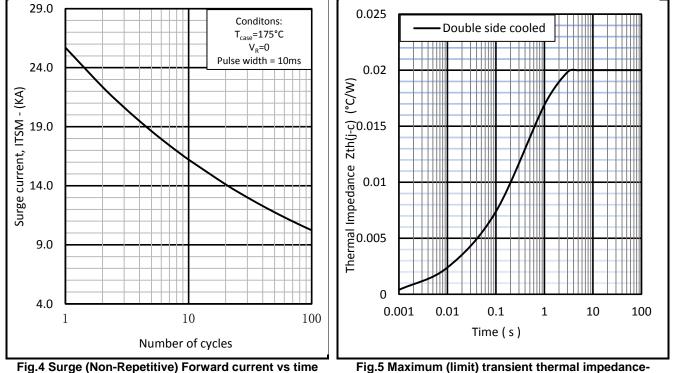
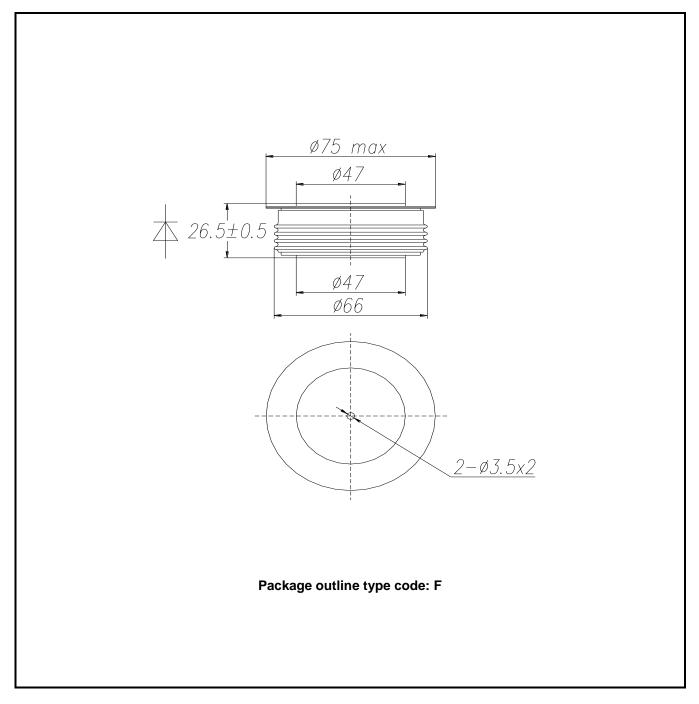


Fig.5 Maximum (limit) transient thermal impedant junction to case

PACKAGE DETAILS

For further package information, please contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



Note:

Some packages may be supplied with gate and or tags.

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