

DRD6990M28

Rectifier Diode

Replaces DS5869-2.0 Jan 2010 - Datasheet DS2904SM28

DS6228-1 February 2018 (LN35173)

FEATURES

- Double Side Cooling
- High Surge Capability

APPLICATIONS

- Rectification
- Free-wheel Diode
- DC Motor Control
- Power Supplies
- Welding
- Battery Chargers

VOLTAGE RATINGS

Part and Ordering Number	Repetitive Peak Voltages V _{DRM} and V _{DRM} V	Conditions
DRD6990M28 DRD6990M26 DRD6990M24	2800 2600 2400	$\begin{array}{l} T_{vj} = -40^{\circ}C \text{ to } 160^{\circ}C, \\ I_{RRM} = 200mA, \\ V_{RRM} t_p = 10ms, \\ V_{RSM} = V_{RRM} + 100V \\ respectively \end{array}$

Lower voltage grades available.

ORDERING INFORMATION

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

DRD6990M26 for a 2600V device

Note: Please use the complete part number when ordering and quote this number in any future correspondence relating to your order.

KEY PARAMETERS

V _{RRM}	2800V
I _{F(AV)}	8790A
I _{FSM}	95000A

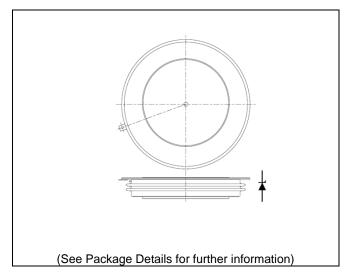


Fig. 1 Package outline M

CURRENT RATINGS

T_{case} = 75°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
Double Si	de Cooled		1	I
I _{F(AV)}	Mean forward current	Half wave resistive load	8790	А
I _{F(RMS)}	RMS value	-	13800	А
I _F	Continuous (direct) on-state current	-	12777	А
Single Sid	Single Side Cooled (Anode side)			
I _{F(AV)}	Mean forward current	Half wave resistive load	5765	А
I _{F(RMS)}	RMS value	-	9056	А
I _F	Continuous (direct) on-state current	-	7698	А

T_{case} = 100°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
Double Si	de Cooled			
I _{F(AV)}	Mean forward current	Half wave resistive load	6992	А
I _{F(RMS)}	RMS value	-	10984	А
I _F	Continuous (direct) on-state current	-	9942	А
Single Sic	le Cooled (Anode side)			
I _{F(AV)}	Mean forward current	Half wave resistive load	4507	А
I _{F(RMS)}	RMS value	-	7079	А
I _F	Continuous (direct) on-state current	-	5857	А

SURGE RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
I _{FSM}	Surge (non-repetitive) on-state current	10ms half sine, T _{case} = 150°C	76.0	kA
l ² t	I ² t for fusing	V_{R} = 50% V_{RRM} - 1⁄4 sine	28.9	MA ² s
I _{FSM}	Surge (non-repetitive) on-state current	10ms half sine, T _{case} = 150°C	95.0	kA
l ² t	I ² t for fusing	$V_R = 0$	45.1	MA ² s

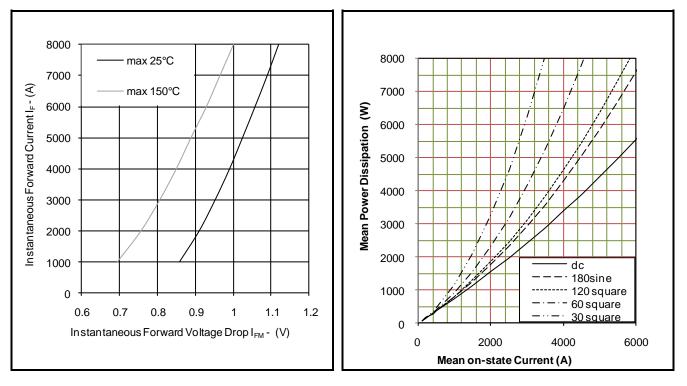
THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Condition	S	Min.	Max.	Units
R _{th(j-c)}	Thermal resistance – junction to case	Double side cooled	DC	-	0.00558	°C/W
		Single side cooled	Anode DC	-	0.01115	°C/W
			Cathode DC	-	0.01115	°C/W
R _{th(c-h)}	Thermal resistance – case to heatsink	Clamping force 54kN	Double side	-	0.00113	°C/W
		(with mounting compound)	Single side	-	0.00226	°C/W
T_{vj}	Virtual junction temperature	On-state (conducting)		-	170	°C
		Reverse (blocking)		-	160	°C
T _{stg}	Storage temperature range			-55	160	°C
F _m	Clamping force			75	91	kN

CHARACTERISTICS

Symbol	Parameter	Test Conditions	Min.	Max.	Units
V_{FM}	Forward voltage	At 3000A peak, T _{case} = 25°C	-	0.97	V
I _{RM}	Peak reverse current	At V _{DRM,} T _{case} = 160°C	-	200	mA
Qs	Total stored charge	I _F = 2000A, dI _{RR} /dt =3A/µs	-	3900	μC
Irr	Peak reverse recovery current	$T_{case} = 150^{\circ}C, V_{R} > 300V$	-	115	А
	Low Level Threshold voltage	At 150°C, 1000A to 3200A	-	0.6364	V
V _{TO}	High Level Threshold voltage	At 150°C, 3200A to 8000A	-	0.6909	V
	Low Level Slope resistance	At 150°C, 1000A to 3200A	-	0.056	mΩ
r _T	High Level Slope resistance	At 150°C, 3200A to 8000A	-	0.0389	mΩ

CURVES



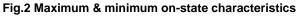
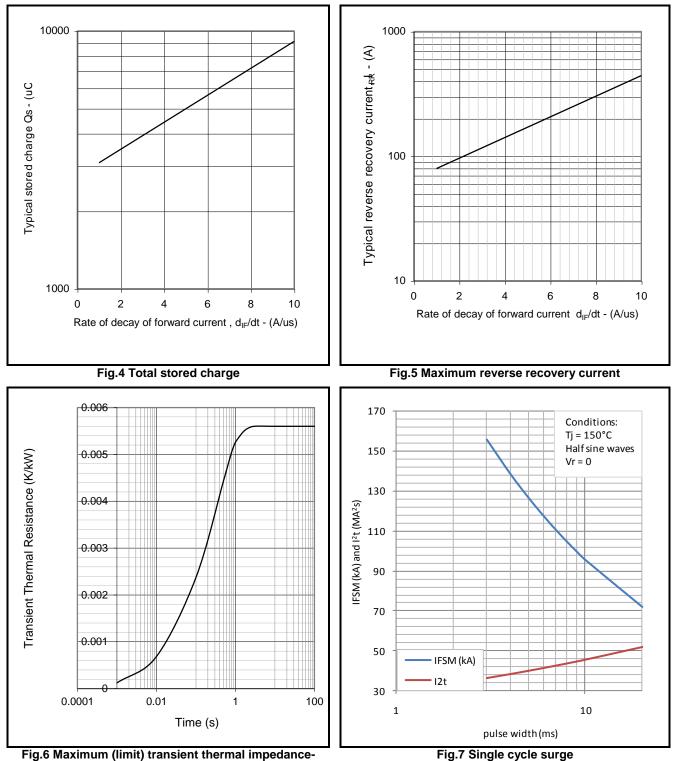


Fig.3 Dissipation curves

V_{TM} EQUATION

Where A = 0.419759 B = 0.029383 C = 0.000023 D = 0.001492 these values are valid for $T_j = 150^{\circ}C$ for I_T 1000A to 8000A

 $V_{TM} = A + BIn (I_T) + C.I_T + D.\sqrt{I_T}$



junction to case

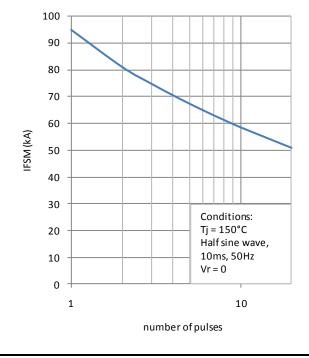


Fig.8 Multi-cycle surge

PACKAGE DETAILS

For further package information, please contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.

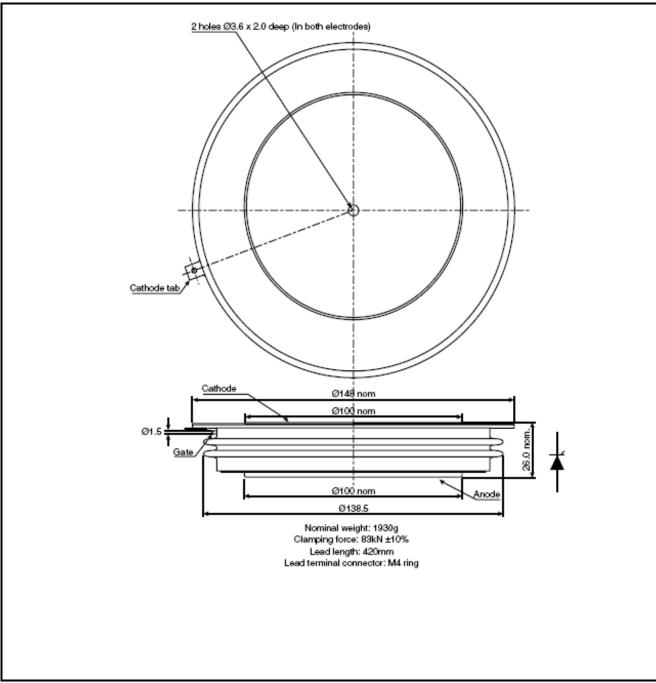


Figure 8 Package outline code M

Note:

Some packages may be supplied with gate and or tags.

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