

FEATURES

- Double Side Cooling
- High Surge Capability

KEY PARAMETERS

V_{RRM}	5000V
$I_{F(AV)}$	710A
I_{FSM}	11500A

VOLTAGE RATINGS

Part and Ordering Number	Repetitive Peak Voltages V_{RRM} V	Conditions
DRD710G50 DRD710G48 DRD710G46	5000 4800 4600	$V_{RSM} = V_{RRM} + 100V$

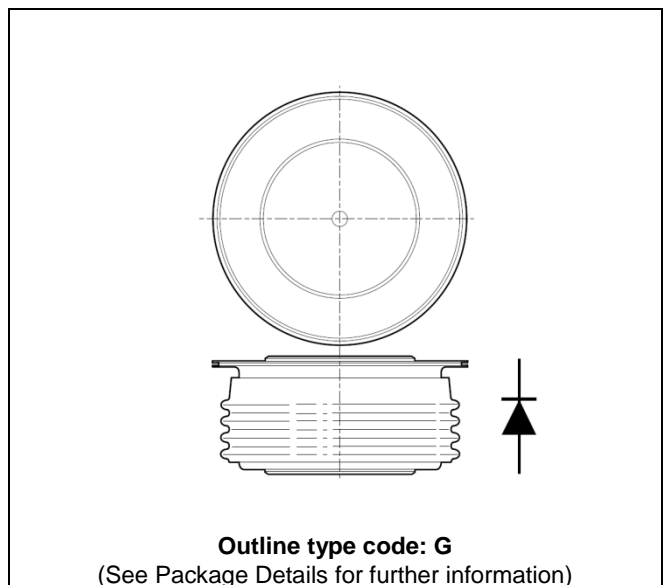


Fig. 1 Package outline

ORDERING INFORMATION

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

DRD710G46 for a 4600V device

CURRENT RATINGS

$T_{case} = 75^{\circ}C$ unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
Double Side Cooled				
$I_{F(AV)}$	Mean forward current	Half wave resistive load	910	A
$I_{F(RMS)}$	RMS value	-	1430	A
I_F	Continuous (direct) on-state current	-	1314	A
Single Side Cooled (Anode side)				
$I_{F(AV)}$	Mean forward current	Half wave resistive load	599	A
$I_{F(RMS)}$	RMS value	-	941	A
I_F	Continuous (direct) on-state current	-	814	A

$T_{case} = 100^{\circ}C$ unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
Double Side Cooled				
$I_{F(AV)}$	Mean forward current	Half wave resistive load	710	A
$I_{F(RMS)}$	RMS value	-	1115	A
I_F	Continuous (direct) on-state current	-	1000	A
Single Side Cooled (Anode side)				
$I_{F(AV)}$	Mean forward current	Half wave resistive load	450	A
$I_{F(RMS)}$	RMS value	-	706	A
I_F	Continuous (direct) on-state current	-	570	A

SURGE RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
I_{FSM}	Surge (non-repetitive) on-state current	10ms half sine, $T_{case} = 150^{\circ}C$ $V_R = 50\% V_{RRM} - \frac{1}{4}$ sine	9.2	kA
I^2t	I^2t for fusing		0.422	MA ² s
I_{FSM}	Surge (non-repetitive) on-state current	10ms half sine, $T_{case} = 150^{\circ}C$ $V_R = 0$	11.5	kA
I^2t	I^2t for fusing		0.66	MA ² s

THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Conditions	Min.	Max.	Units	
$R_{th(j-c)}$	Thermal resistance – junction to case	Double side cooled	DC	-	0.032	$^{\circ}C/W$
		Single side cooled	Anode DC	-	0.064	$^{\circ}C/W$
			Cathode DC	-	0.064	$^{\circ}C/W$
$R_{th(c-h)}$	Thermal resistance – case to heatsink	Clamping force 12kN (with mounting compound)	Double side	-	0.008	$^{\circ}C/W$
			Single side	-	0.016	$^{\circ}C/W$
T_{vj}	Virtual junction temperature	On-state (conducting)		-	160	$^{\circ}C$
		Reverse (blocking)		-	150	$^{\circ}C$
T_{stg}	Storage temperature range			-55	175	$^{\circ}C$
F_m	Clamping force			11.5	13.5	kN

CHARACTERISTICS

Symbol	Parameter	Test Conditions	Min.	Max.	Units
V _{FM}	Forward voltage	At 1800A peak, T _{case} = 25°C	-	1.8	V
I _{RM}	Peak reverse current	At V _{RRM} , T _{case} = 150°C	-	50	mA
Q _S	Total stored charge	I _F = 1000A, dI _{RR} /dt = 3A/μs	-	2600	μC
I _{rr}	Peak reverse recovery current	T _{case} = 150°C, V _R = 100V	-	80	A
V _{TO}	Threshold voltage	At T _{vj} = 150°C	-	0.88	V
r _T	Slope resistance	At T _{vj} = 150°C	-	0.687	mΩ

CURVES

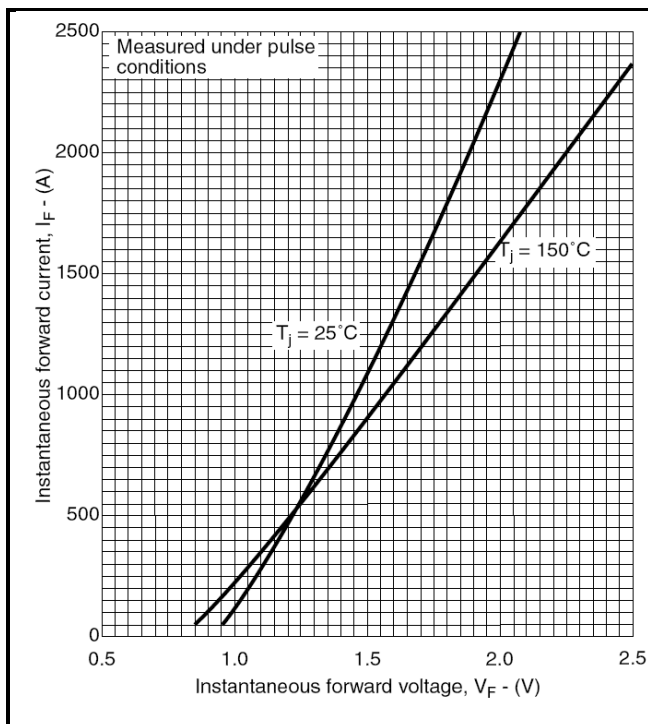


Fig.2 Maximum (limit) on-state characteristics

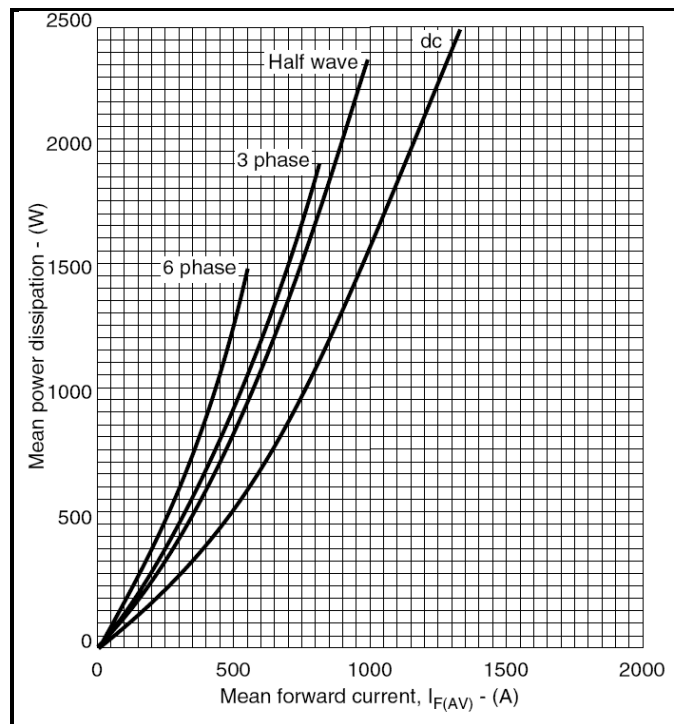


Fig.3 Dissipation curves

V_{TM} EQUATION

$$V_{TM} = A + B \ln(I_T) + C \cdot I_T + D \cdot \sqrt{I_T}$$

Where A = 1.183601
 B = -0.13593
 C = 0.000384
 D = 0.030400

these values are valid for T_j = 150°C for I_F 100A to 2500A

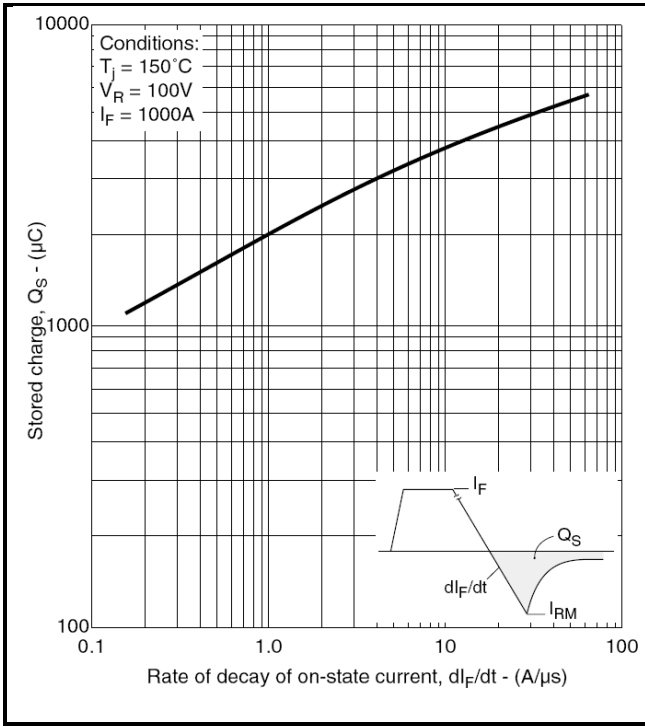


Fig.4 Total stored charge

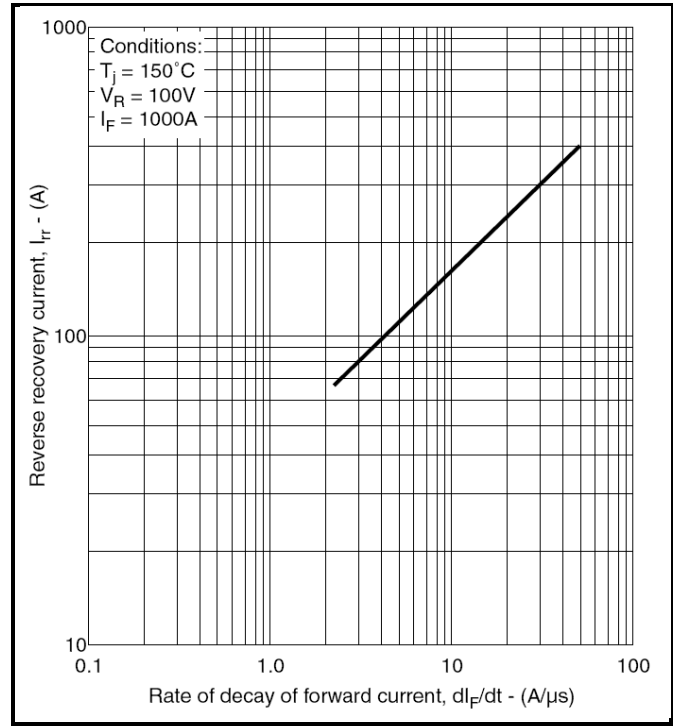


Fig.5 Maximum reverse recovery current

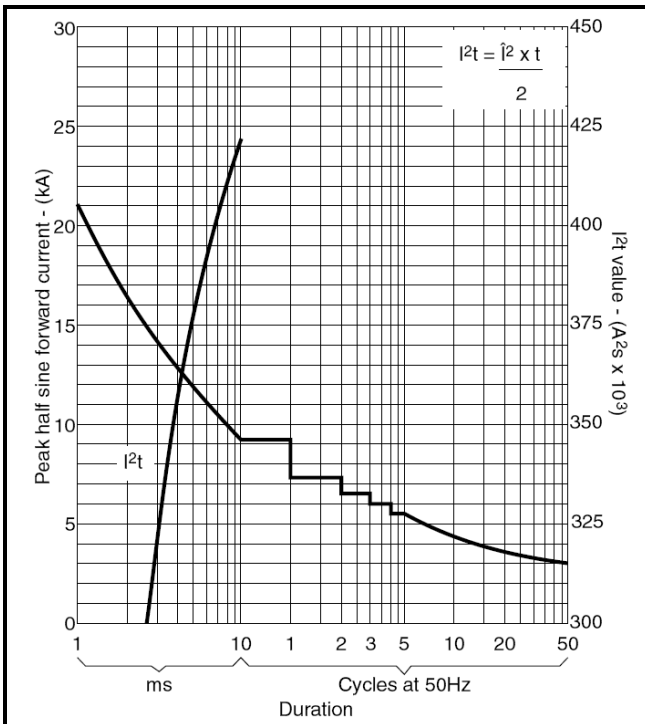


Fig.6 Surge (non-repetitive) forward current vs time (with 50% V_{RRM} at $T_{case} 150^\circ\text{C}$)

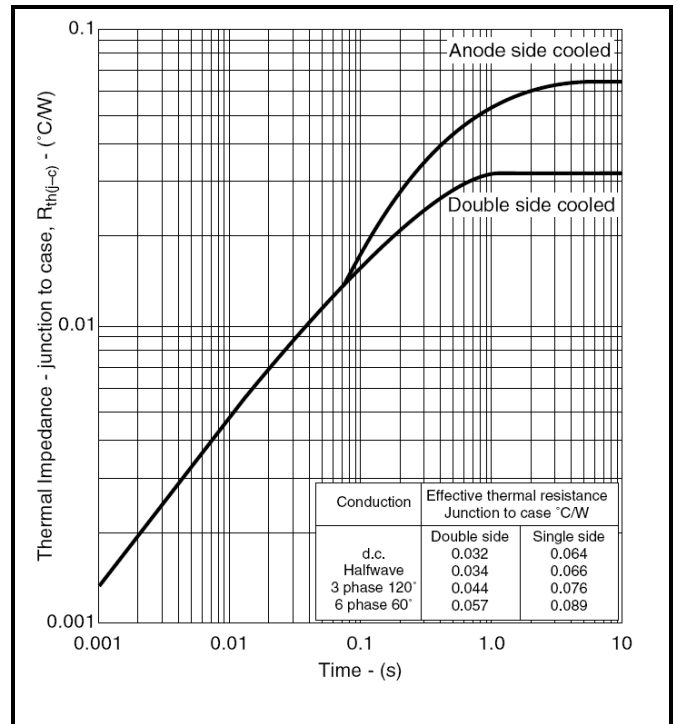
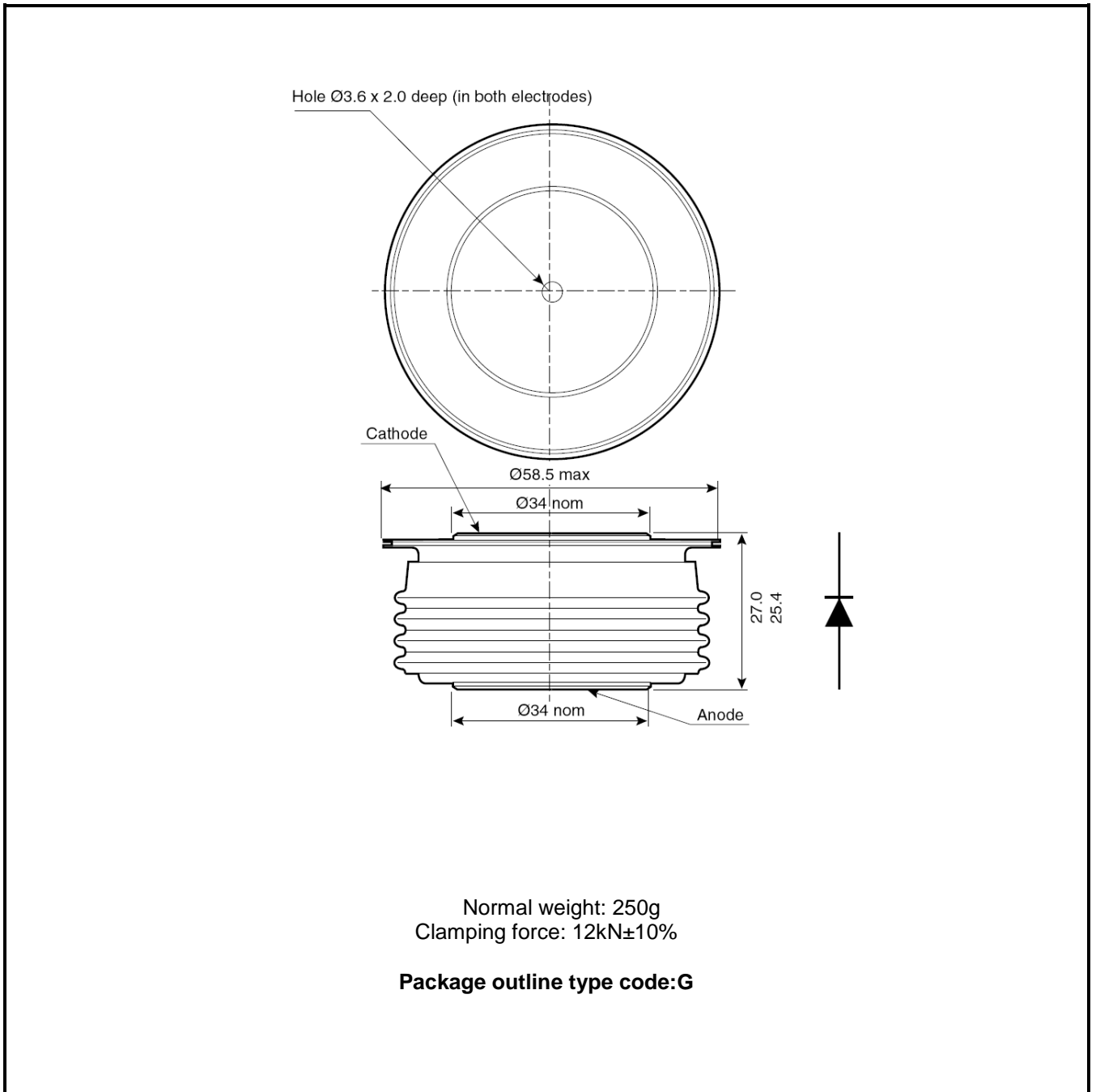


Fig.7 Maximum (limit) transient thermal impedance-junction to case

PACKAGE DETAILS

For further package information, please contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



Note:
Some packages may be supplied with gate and or tags.

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